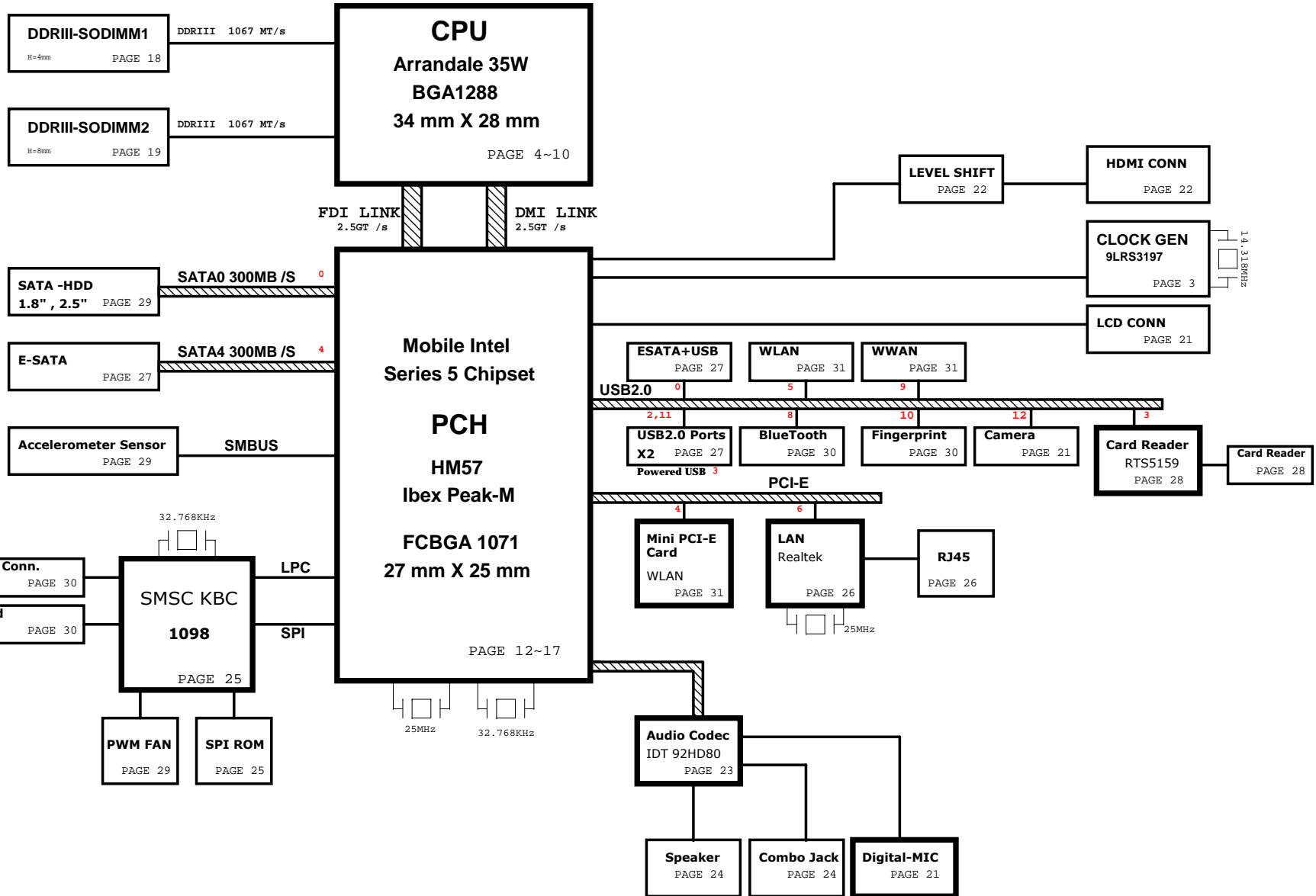


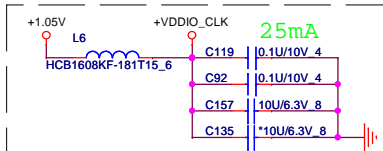
LAYER 1 : TOP
LAYER 2 : SGND
LAYER 3 : IN1
LAYER 4 : IN3/SGND
LAYER 5 : SVCC
LAYER 7 : IN2
LAYER 7 : SGND
LAYER 8 : BOT



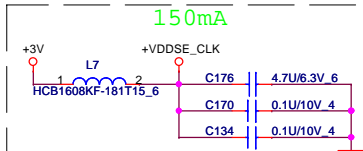
- 3/5V RT8206B
PAGE 36
- VCCP +1.1VTT(RT8208A) AND DDR III SMDDR_VTERM 1.5V/1.5VSUS(RT8207)
PAGE 38
- CPU CORE ADP3212
PAGE 37
- VGACORE RT8152C
PAGE 41
- PCH 1.05V RT8204C
PAGE 39
- SYSTEM CHARGER(bq24740)
PAGE 34, 35

power State	+RTC_CELL	+VIN +3VPCU +5VPCU	+3VS5 +5VS5	+5VSUS +1.5VSUS	+5V +3V +1.8V +1.5V +1.5V_CPU +1.1V_VTT +1.05V +VGACORE_IGPU +VCORE
S0	ON	ON	ON	ON	ON
S1	ON	ON	ON	ON	ON
S3	ON	ON	ON	ON	OFF
S4/S5 AC	ON	ON	ON	OFF	OFF
S4/S5 DC Only	ON	ON	OFF	OFF	OFF
AC/DC No Exist	ON	OFF	OFF	OFF	OFF

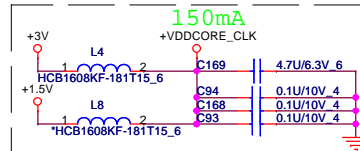
	SOURCE	BATTERY	CLK GEN	Thermal IC	G-SENSOR	WLAN	SO-DIMM	PCH
SMBCLK SMBDATA	PCH	X	Y	Y	Y	Y	Y	X
AB2A_CLK AB2B_DATA	SMSC 1098	X	X	X	X	X	X	Y
AB1A_CLK AB1A_DATA	SMSC 1098	Y	X	X	X	X	X	X



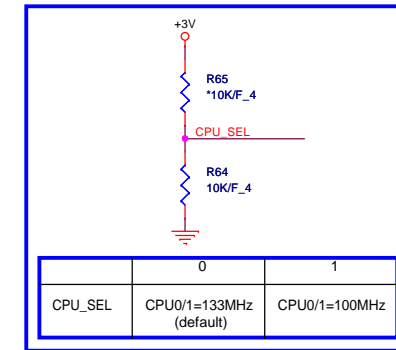
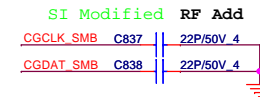
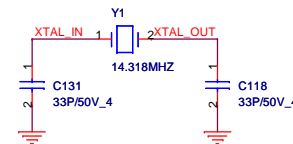
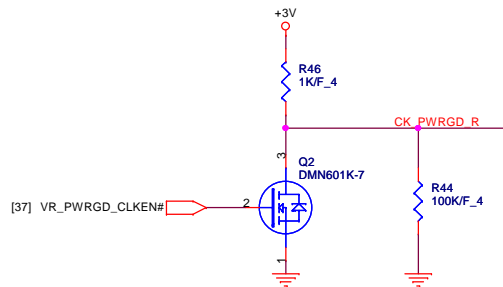
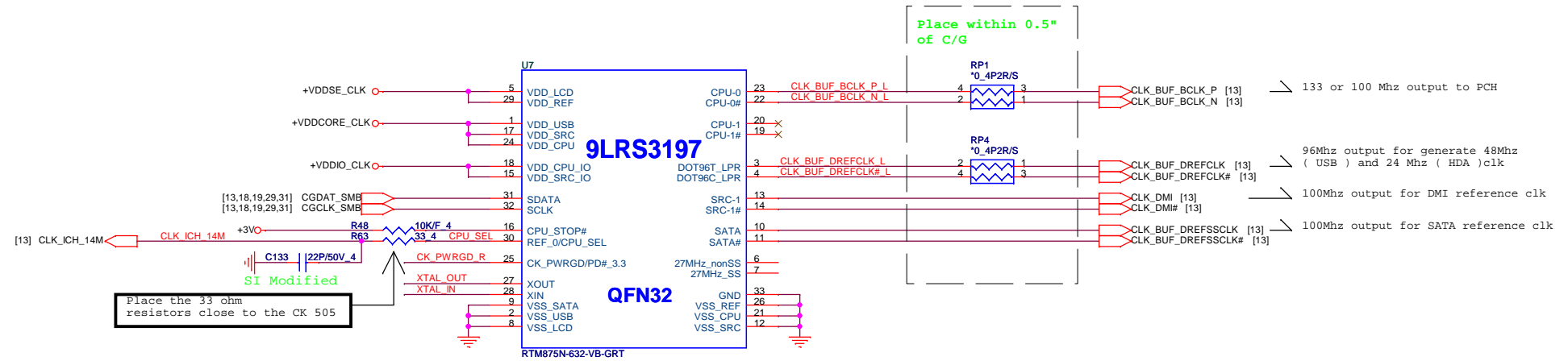
Place each 0.1uF cap as close as possible to each VDD IO pin. Place the 10uF caps on the VDD_IO plane.

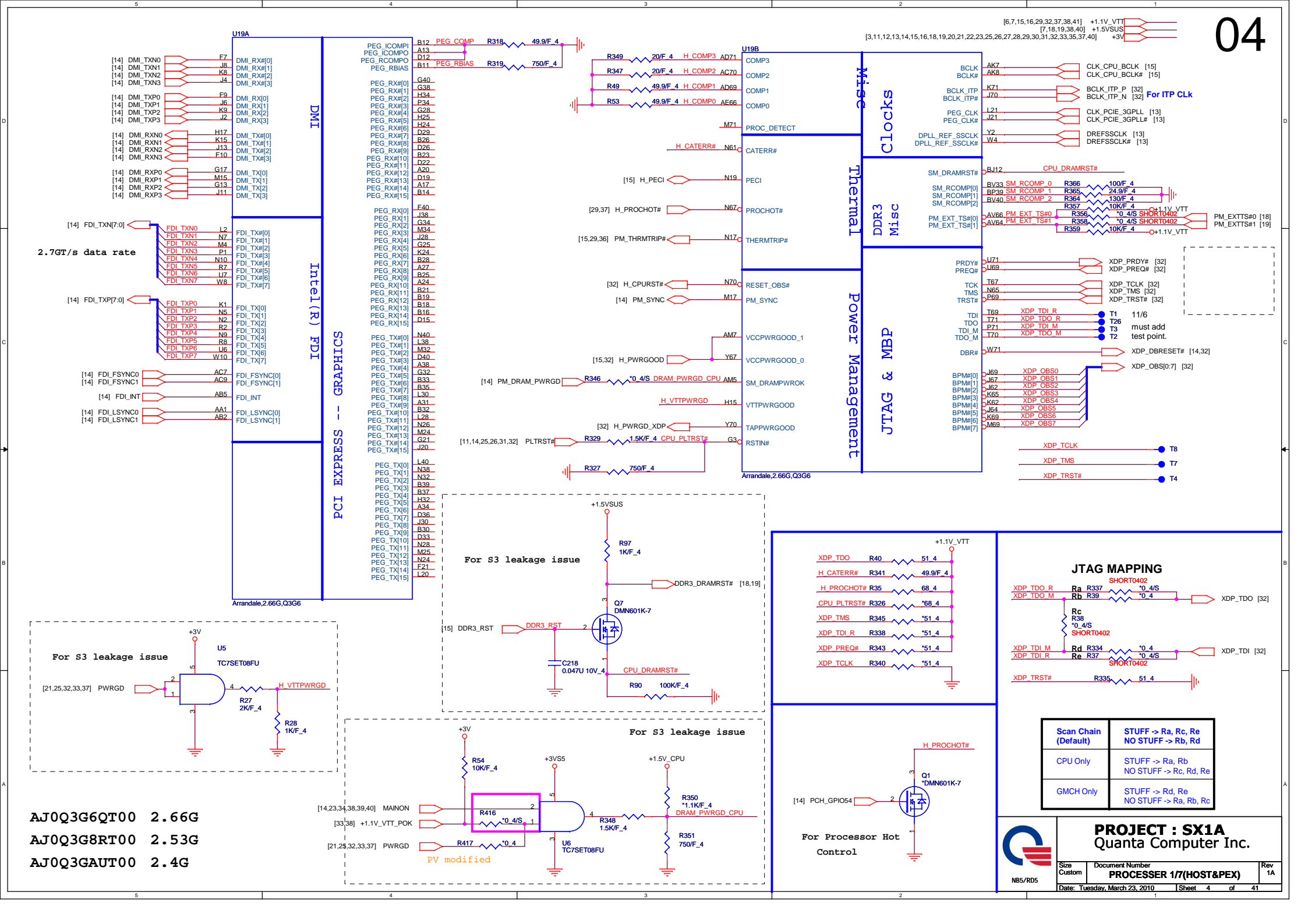


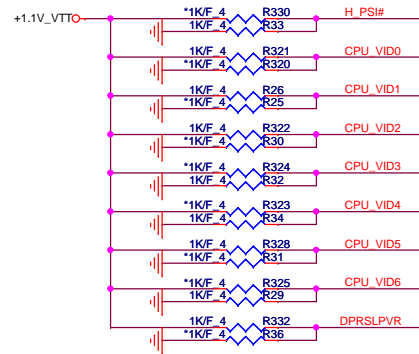
Place each 0.1uF cap close to pin



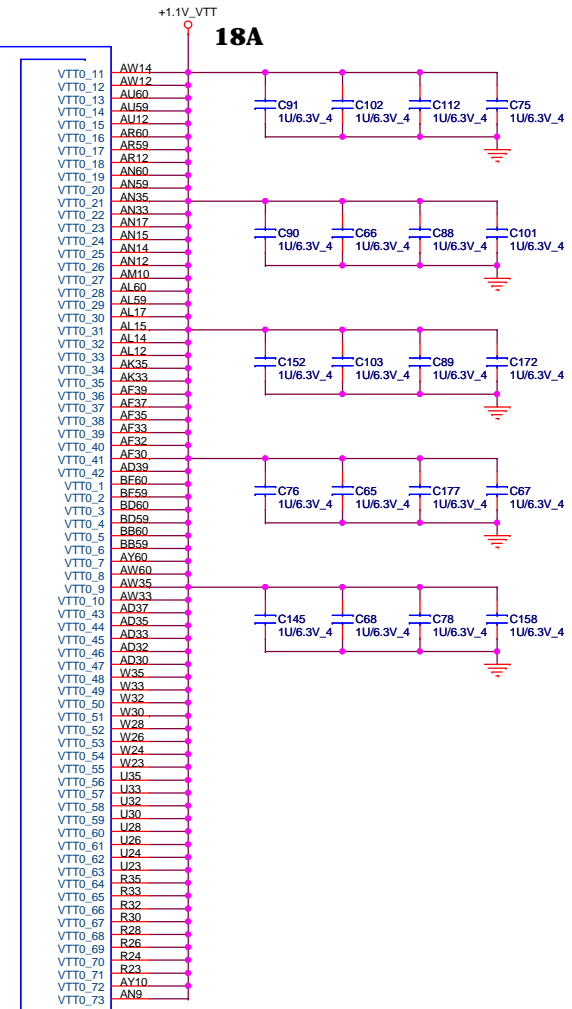
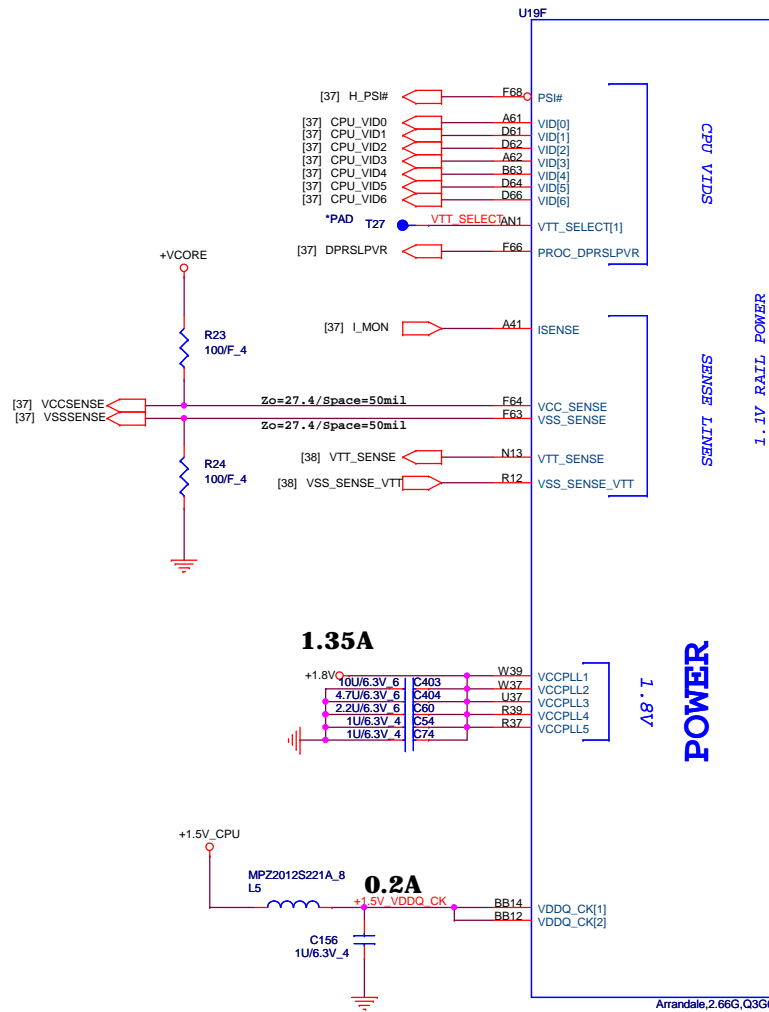
Place each 0.1uF cap close to pin







HFM_VID : Max 1.4V
LFM_VID : Min 0.65V

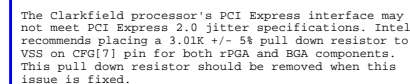


PROJECT : SX1A
Quanta Computer Inc.

Size	Document Number	Rev
Custom	PROCESSOR 3/7(Power1)	1A
Date: Tuesday, March 23, 2010	Sheet 6 of 41	

Size Custom	Document Number PROCESSER 4/7(Power2)	Rev 1/A
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	1	0
CFG4 (Display Port Presence)	Disabled; No Physical Display Port attached to Embedded Display Port	Enabled; An external Display port device is connected to the Embedded Display port
CFG0 (PCI-Epress Configuration Select)	Single PEG	Bifurcation enabled
CFG3 (PCI-Epress Static Lane Reversal)	Normal Operation	Lane Numbers Reversed 15 > 0 , 14 > 1


ARRANDALE PROCESSOR (GND)

U19I		
BU62	VSS1	VSS90
BU68	VSS2	VSS91
BU65	VSS3	VSS92
BU51	VSS4	VSS93
BU48	VSS5	VSS94
BU44	VSS6	VSS95
BU37	VSS7	VSS96
BU32	VSS8	VSS97
BU25	VSS9	VSS98
BU21	VSS10	VSS99
BU18	VSS11	VSS100
BU14	VSS12	VSS101
BU11	VSS13	VSS102
BU7	VSS14	VSS103
BP42	VSS15	VSS104
BN64	VSS16	VSS105
BN6	VSS17	VSS106
BM70	VSS18	VSS107
BM51	VSS19	VSS108
BM44	VSS20	VSS109
BM32	VSS21	VSS110
BM24	VSS22	VSS111
BM17	VSS23	VSS112
BL57	VSS24	VSS113
BL55	VSS25	VSS114
BL48	VSS26	VSS115
BL40	VSS27	VSS116
BL28	VSS28	VSS117
BL20	VSS29	VSS118
BK63	VSS30	VSS119
BK60	VSS31	VSS120
BK53	VSS32	VSS121
BK34	VSS33	VSS122
BK10	VSS34	VSS123
BJ64	VSS35	VSS124
BJ21	VSS36	VSS125
BJ9	VSS37	VSS126
BJ1	VSS38	VSS127
BH70	VSS39	VSS128
BH57	VSS40	VSS129
BH55	VSS41	VSS130
BH47	VSS42	VSS131
BH24	VSS43	VSS132
BH20	VSS44	VSS133
BH15	VSS45	VSS134
BG51	VSS46	VSS135
BG36	VSS47	VSS136
BF62	VSS48	VSS137
BF30	VSS49	VSS138
BF13	VSS50	VSS139
BF8	VSS51	VSS140
BE70	VSS52	VSS141
BE65	VSS53	VSS142
BE9	VSS54	VSS143
BE1	VSS55	VSS144
BD57	VSS56	VSS145
BD53	VSS57	VSS146
BD50	VSS58	VSS147
BD46	VSS59	VSS148
BD42	VSS60	VSS149
BD39	VSS61	VSS150
BD14	VSS62	VSS151
BB71	VSS63	VSS152
BB62	VSS64	VSS153
BB57	VSS65	VSS154
BB53	VSS66	VSS155
BB50	VSS67	VSS156
BB46	VSS68	VSS157
BB42	VSS69	VSS158
BB39	VSS70	VSS159
BB7	VSS71	VSS160
BB1	VSS72	VSS161
BA70	VSS73	VSS162
AY71	VSS74	VSS163
AY66	VSS75	VSS164
AY62	VSS76	VSS165
AY59	VSS77	VSS166
AY55	VSS78	VSS167
AY51	VSS79	VSS168
AY48	VSS80	VSS169
AR42	VSS140	VSS170
AR39	VSS141	VSS171
AR35	VSS142	VSS172
AR33	VSS143	VSS173
AR32	VSS144	VSS174
AR30	VSS145	VSS175
AR28	VSS146	VSS176
AR26	VSS147	VSS177
AR24	VSS148	VSS178
AR23	VSS149	VSS179
AR21	VSS150	VSS180
AR19	VSS151	VSS181
AR17	VSS152	VSS182
AR15	VSS153	VSS183
AR14	VSS154	VSS184
AR4	VSS155	VSS185
AR1	VSS156	VSS186
AP70	VSS157	VSS187
AP64	VSS158	VSS188
AN62	VSS159	VSS189
AN55	VSS160	VSS190
AY44	VSS213	VSS191
AY35	VSS214	VSS192
AY33	VSS215	VSS193
AY32	VSS216	VSS194
AY30	VSS217	VSS195
AY28	VSS218	VSS196
AY26	VSS219	VSS197
VSS89	VSS220	VSS198

Arrandale,2.66G,Q3G6

U19J		
AH53	VSS202	VSS404
AH51	VSS203	VSS405
AH50	VSS204	VSS406
AH48	VSS205	VSS407
AH46	VSS206	VSS408
AH44	VSS207	VSS409
AH42	VSS208	VSS410
AH41	VSS209	VSS411
AH39	VSS210	VSS412
AH37	VSS211	VSS413
AH35	VSS212	VSS414
AH33	VSS213	VSS415
AH32	VSS214	VSS416
AH30	VSS215	VSS417
AH28	VSS216	VSS418
AH26	VSS217	VSS419
AH24	VSS218	VSS420
AH23	VSS219	VSS421
AH21	VSS220	VSS422
AH19	VSS221	VSS423
AH17	VSS222	VSS424
AH15	VSS223	VSS425
AH14	VSS224	VSS426
AG64	VSS225	VSS427
AG9	VSS226	VSS428
AG6	VSS227	VSS429
AF69	VSS228	VSS430
AF62	VSS229	VSS431
AF1	VSS230	VSS432
AE70	VSS231	VSS433
AE64	VSS232	VSS434
AD62	VSS233	VSS435
AD57	VSS234	VSS436
AD53	VSS235	VSS437
AD50	VSS236	VSS438
AD46	VSS237	VSS439
AD42	VSS238	VSS440
AD4	VSS239	VSS441
AC67	VSS240	VSS442
AC64	VSS241	VSS443
AC10	VSS242	VSS444
AC5	VSS243	VSS445
AC1	VSS244	VSS446
AB70	VSS245	VSS447
AB62	VSS246	VSS448
AB57	VSS247	VSS449
AB53	VSS248	VSS450
AB50	VSS249	VSS451
AB46	VSS250	VSS452
AB42	VSS251	VSS453
AB39	VSS252	VSS454
AB37	VSS253	VSS455
AB35	VSS254	VSS456
AB33	VSS255	VSS457
AB32	VSS256	VSS458
AB30	VSS257	VSS459
AB28	VSS258	VSS460
AB26	VSS259	VSS461
AB24	VSS260	VSS462
AB23	VSS261	VSS463
AB21	VSS262	VSS464
AB19	VSS263	VSS465
AB17	VSS264	VSS466
AB15	VSS265	VSS467
AB14	VSS266	VSS468
AB9	VSS267	VSS469
AA66	VSS268	VSS470
AA64	VSS269	VSS471
AA62	VSS270	VSS472
AA57	VSS271	VSS473
AA53	VSS272	VSS474
AA50	VSS273	VSS475
AA46	VSS274	VSS476
AA42	VSS275	VSS477
AA39	VSS276	VSS478
AA37	VSS277	VSS479
AA35	VSS278	VSS480
AA33	VSS279	VSS481
AA32	VSS280	VSS482
AA30	VSS281	VSS483
AA28	VSS282	VSS484
AA26	VSS283	VSS485
AA24	VSS284	VSS486
AA23	VSS285	VSS487
AA21	VSS286	VSS488
AA19	VSS287	VSS489
F20	VSS288	VSS490
F4	VSS289	VSS491
E37	VSS290	VSS492
E33	VSS291	VSS493
E30	VSS292	VSS494
E16	VSS293	VSS495
E12	VSS294	VSS496
D41	VSS295	VSS497
D38	VSS296	VSS498
D34	VSS297	VSS499
D31	VSS298	VSS500
D27	VSS299	VSS501
D24	VSS300	VSS502
D20	VSS301	VSS503
D17	VSS302	VSS504
D13	VSS303	VSS505
D10	VSS304	VSS506
D6	VSS305	VSS507
B65	VSS306	VSS508
B40	VSS307	VSS509
VSS415	VSS308	VSS510

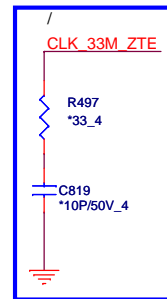
Arrandale,2.66G,Q3G6

		PROJECT : SX1A	
NBS/RD5		Quanta Computer Inc.	
Size Custom	Document Number	PROCESSER 777(GND)	
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SI modified

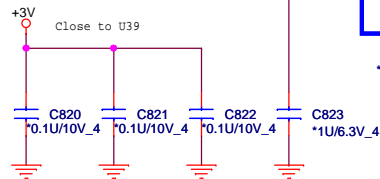
BADD1 BADD0

0	0	EE/EF
0	1	7E/7F
1	0	2E/2F
1	1	4E/4F(Default)



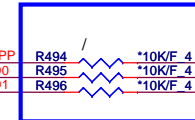
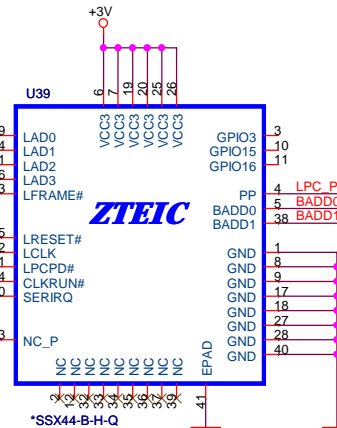
[12,25,31] LAD0
[12,25,31] LAD1
[12,25,31] LAD2
[12,25,31] LAD3
[12,25,31] LFRAME#

[4,14,25,26,31,32] PLTRST#
[14] CLK_33M_ZTE
[14] LPC_PD#
[14,25] CLKRUN#
[12,25] SERIRQ



LAD0 29
LAD1 24
LAD2 21
LAD3 16
LFRAME# 23

PLTRST# 15
CLK_33M_ZTE 22
LCLK 31
CLKRUN# 14
SERIRQ 30



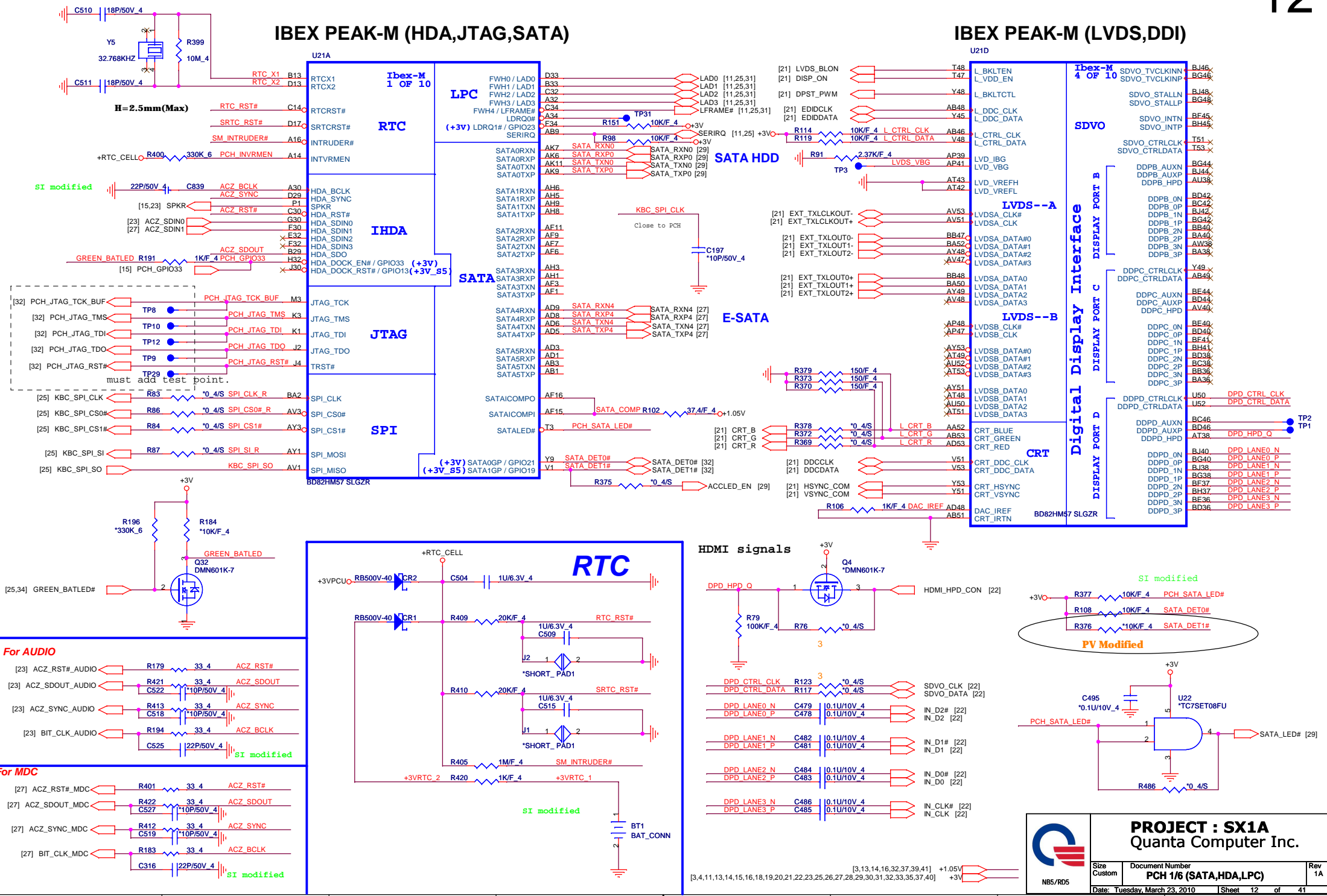
NB5/RD5

PROJECT : SX1A
Quanta Computer Inc.

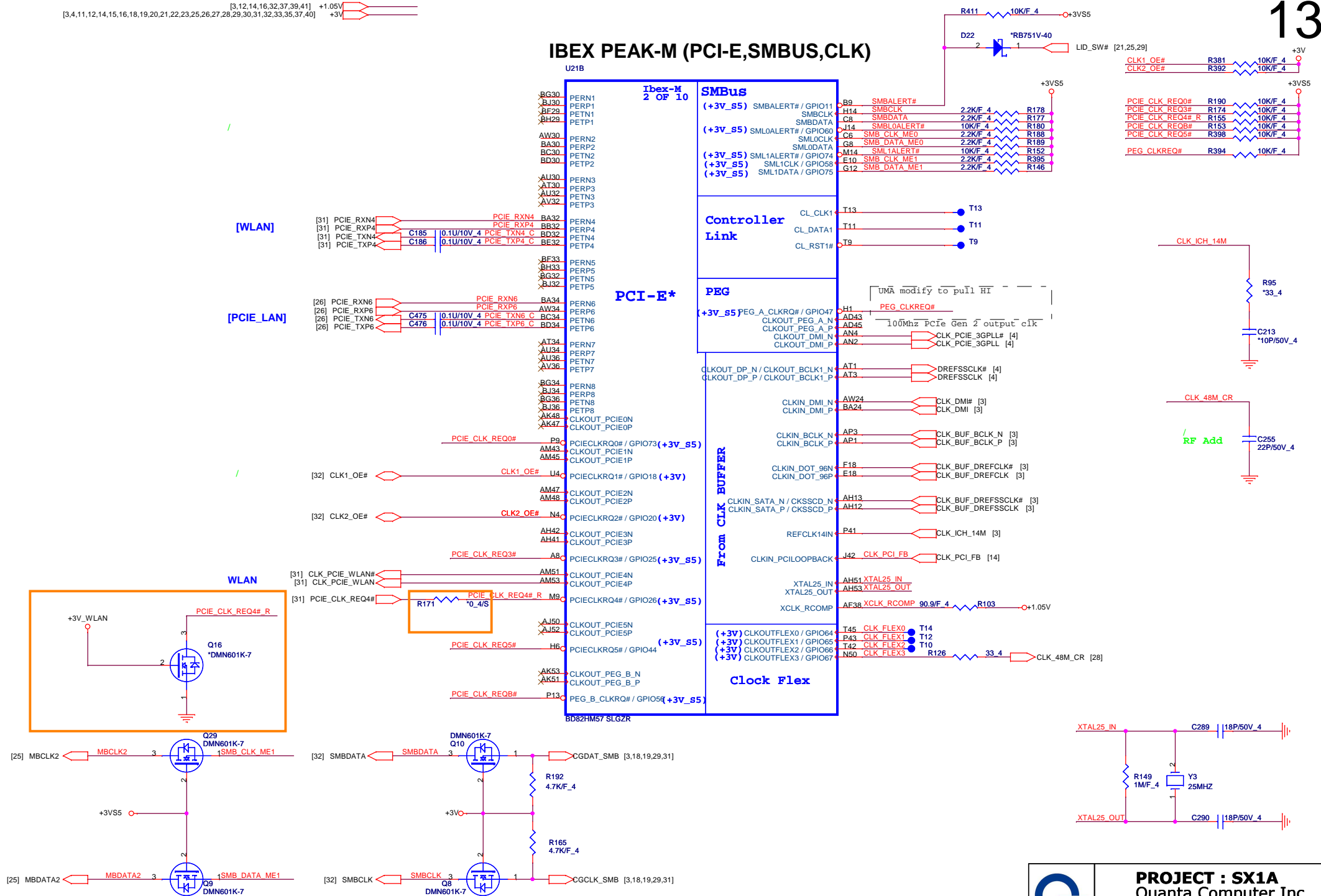
Size B	Document Number TCM(SSX44-B-H-Q)	Rev 1A
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IBEX PEAK-M (HDA,JTAG,SATA)

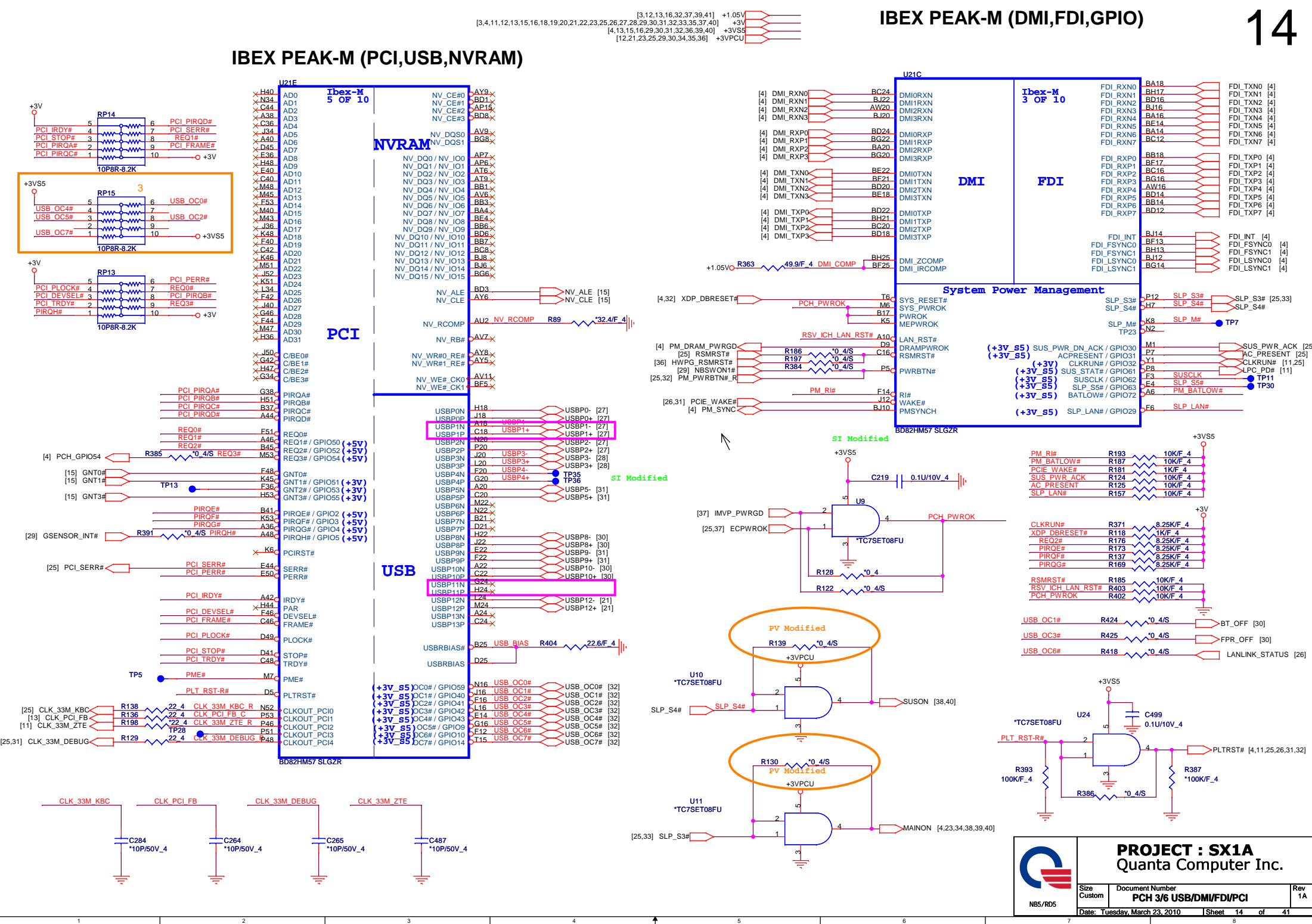
IBEX PEAK-M (LVDS,DDI)



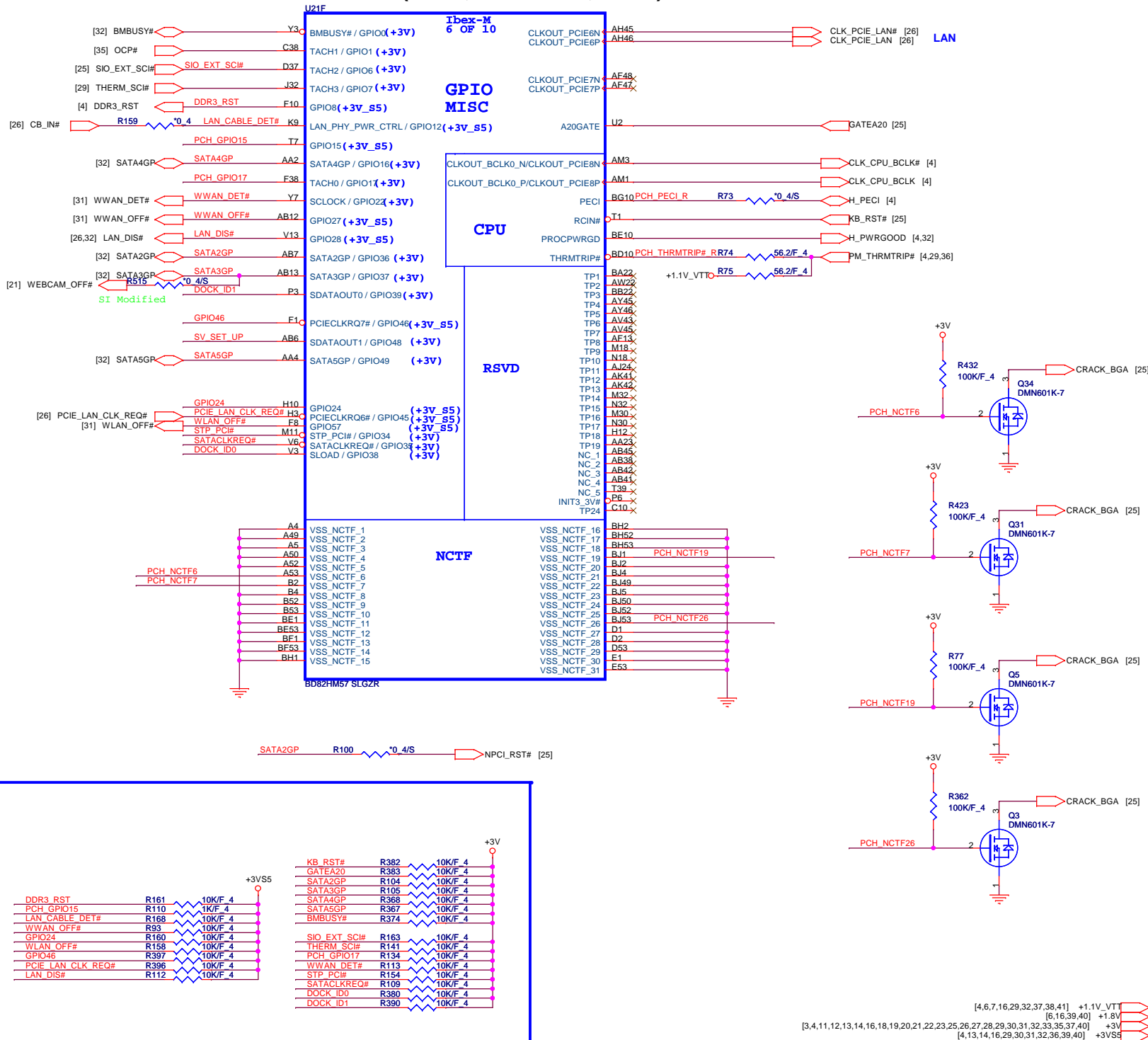
IBEX PEAK-M (PCI-E,SMBUS,CLK)



IBEX PEAK-M (PCI,USB,NVRAM)



IBEX PEAK-M (GPIO,VSS_NCTF,RSVD)



[14] GNT3# 

A16 swap override Strap/Top-Block
Swap Override jumper

GNT3#

```
Low = A16 swap
override/Top-Block
Swap Override enabled
High = Default
```

SV SET UP R107 10K/F_4 +3V

SV_SET

1-X High = Strong (Default)

[14] G

[14] GNT0# R135 *1K/F 4
[14] GNT1# R133 *1K/F 4

[14] G

Boot BIOS Strap

PCI_GNT0#	GNT#1	Boot BIOS Location
0	0	LPC
0	1	Reserved (NAND)
1	0	PCI
1	1	SPI

[14] NV_ALE
[14] NV_CLE

R78
R85

*1K/F_4
*1K/F_4

+1.8V

Danbury Technology Enabled

NV_ALE	High = Enable Low = Disable
--------	--------------------------------

DMI Termination Voltage

NV_CLE	Set to Vcc when LOW Set to Vcc/2 when HIGH
--------	---

No Reboot Strap

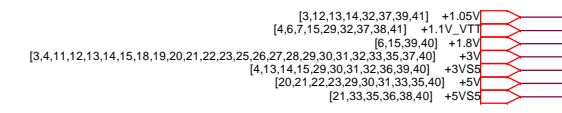
[12,23] SPKR SPKR R389 *1K/F 4 +3V

[12] PCH_GPIO33 R156 *100K/F 4



PROJECT : SX1A
Quanta Computer Inc.

Size Custom	Document Number PCH 4/6 (GPIO & Strap)	Rev 1A
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IBEX PEAK-M (GND)

IBEX PEAK-M (GND)

17

U21L		
AY7	VSS[159]	H49
B11	VSS[160]	H5
B15	VSS[161]	J24
B19	VSS[162]	K11
B23	VSS[163]	K43
B31	VSS[164]	K47
B35	VSS[165]	K7
B39	VSS[166]	L14
B43	VSS[167]	L18
B47	VSS[168]	L2
B7	VSS[169]	L22
BG12	VSS[170]	L32
BB12	VSS[171]	L36
BB16	VSS[172]	L40
BB20	VSS[173]	L52
BB24	VSS[174]	M12
BB30	VSS[175]	M16
BB34	VSS[176]	M20
BB38	VSS[177]	N38
BB42	VSS[178]	M34
BB46	VSS[179]	M38
BB5	VSS[180]	M42
BC10	VSS[181]	M46
BC14	VSS[182]	M49
BC18	VSS[183]	M5
BC2	VSS[184]	M8
BC22	VSS[185]	N24
BC32	VSS[186]	P11
BC36	VSS[187]	P22
BC40	VSS[188]	P30
BC44	VSS[189]	P32
BC52	VSS[190]	P34
BH9	VSS[191]	P42
BD48	VSS[192]	P45
BD49	VSS[193]	P47
BD5	VSS[194]	R2
BE12	VSS[195]	R52
BE16	VSS[196]	T12
BE20	VSS[197]	T41
BE24	VSS[198]	T46
BE30	VSS[199]	T49
BE34	VSS[200]	T5
BE38	VSS[201]	T8
BE42	VSS[202]	U30
BE46	VSS[203]	U31
BE48	VSS[204]	U32
BE50	VSS[205]	U34
BE6	VSS[206]	P38
BE8	VSS[207]	V11
BF3	VSS[208]	P16
BF49	VSS[209]	V19
BF51	VSS[210]	V20
BG18	VSS[211]	V22
BG24	VSS[212]	V30
BG4	VSS[213]	V31
BG50	VSS[214]	V32
BH11	VSS[215]	V34
BH15	VSS[216]	V35
BH19	VSS[217]	V38
BH23	VSS[218]	V43
BH31	VSS[219]	V45
BH35	VSS[220]	V46
BH39	VSS[221]	V47
BH43	VSS[222]	V49
BH47	VSS[223]	V5
BH7	VSS[224]	V7
C12	VSS[225]	V8
C50	VSS[226]	W2
D51	VSS[227]	W52
E12	VSS[228]	Y11
E16	VSS[229]	Y12
E20	VSS[230]	Y15
E24	VSS[231]	Y19
E30	VSS[232]	Y23
E34	VSS[233]	Y28
E38	VSS[234]	Y30
E42	VSS[235]	Y31
E46	VSS[236]	Y32
E48	VSS[237]	Y38
E6	VSS[238]	Y43
E8	VSS[239]	Y46
F49	VSS[240]	P49
F5	VSS[241]	Y5
G10	VSS[242]	Y6
G14	VSS[243]	Y8
G18	VSS[244]	P24
G2	VSS[245]	T43
G22	VSS[246]	AD51
G32	VSS[247]	AT8
G36	VSS[248]	AD47
G40	VSS[249]	Y47
G44	VSS[250]	AT12
G52	VSS[251]	AM6
AF39	VSS[252]	AT13
H16	VSS[253]	AM5
H20	VSS[254]	AK45
H30	VSS[255]	AK39
H34	VSS[256]	AV14
H38	VSS[257]	
H42	VSS[258]	

BD82HM57 SLGZR

U21H		
AB16	VSS[0]	AK30
AA19	VSS[1]	AK31
AA20	VSS[2]	AK32
AA22	VSS[3]	AK34
AM19	VSS[4]	AK35
AA24	VSS[5]	AK38
AA26	VSS[6]	AK43
AA28	VSS[7]	AK46
AA30	VSS[8]	AK49
AA31	VSS[9]	AK5
AA32	VSS[10]	AK8
AB11	VSS[11]	AL2
AB15	VSS[12]	AL52
AB23	VSS[13]	AM11
AB30	VSS[14]	AM20
AB31	VSS[15]	AM22
AB32	VSS[16]	AM24
AB39	VSS[17]	AM26
AB43	VSS[18]	AM29
AB47	VSS[19]	BA42
AB5	VSS[20]	AM30
AB8	VSS[21]	AM31
AC2	VSS[22]	AM32
AC52	VSS[23]	AM34
AD11	VSS[24]	AM35
AD12	VSS[25]	AM38
AD16	VSS[26]	AM42
AD23	VSS[27]	AU20
AD30	VSS[28]	AM46
AD31	VSS[29]	AV22
AD32	VSS[30]	AM49
AD34	VSS[31]	AM7
AU22	VSS[32]	AA50
AD42	VSS[33]	BB10
AD46	VSS[34]	AN32
AD49	VSS[35]	AN50
AD7	VSS[36]	AN52
AE2	VSS[37]	AP12
AE4	VSS[38]	AP42
Y13	VSS[39]	AP46
AH49	VSS[40]	AP49
AF13	VSS[41]	AP5
AN34	VSS[42]	AP8
AF35	VSS[43]	AR2
AF45	VSS[44]	AR52
AF46	VSS[45]	AT11
AF49	VSS[46]	BA12
AF5	VSS[47]	AH48
AF8	VSS[48]	AT32
AG2	VSS[49]	AT36
AG52	VSS[50]	AT41
AH11	VSS[51]	AT47
AH15	VSS[52]	AT7
AH16	VSS[53]	AV12
AH24	VSS[54]	AV16
AH32	VSS[55]	AV20
AV18	VSS[56]	AV24
AH43	VSS[57]	AV30
VSS[59]	VSS[58]	AV34
AH47	VSS[59]	AV38
AH7	VSS[60]	AV42
AJ19	VSS[61]	AV46
AJ2	VSS[62]	AV49
AJ20	VSS[63]	AV5
AJ22	VSS[64]	AV8
AJ23	VSS[65]	AW14
AJ26	VSS[66]	AW18
AJ28	VSS[67]	AW2
AJ32	VSS[68]	BF9
AJ34	VSS[69]	AW32
AT5	VSS[70]	AW36
AJ4	VSS[71]	AW40
AK12	VSS[72]	AW54
AM41	VSS[73]	AY11
AN19	VSS[74]	AY43
AK26	VSS[75]	AY47
AK22	VSS[76]	
AK23	VSS[77]	
AK28	VSS[78]	
	VSS[79]	

BD82HM57 SLGZR

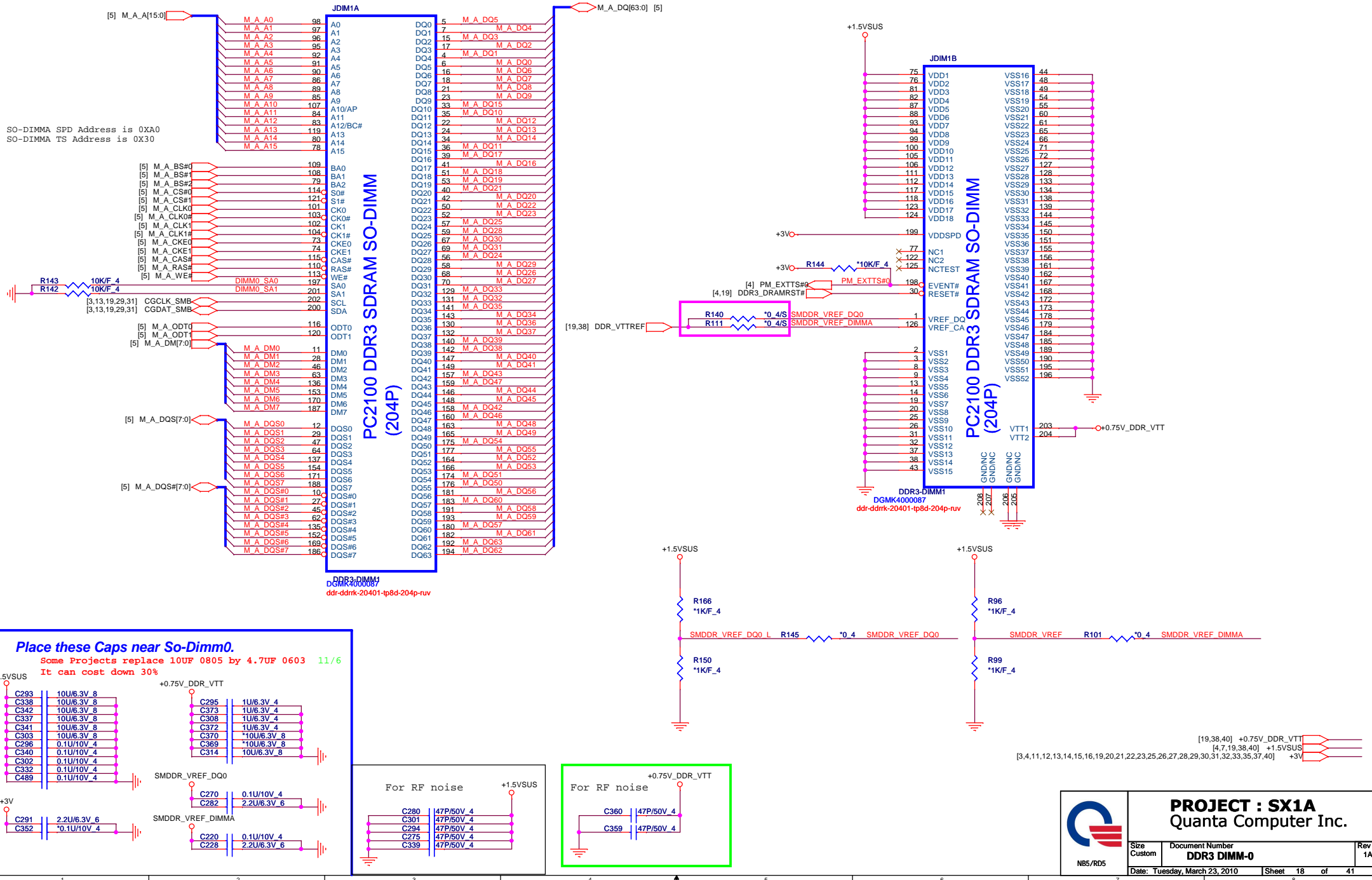


PROJECT : SX1A
Quanta Computer Inc.

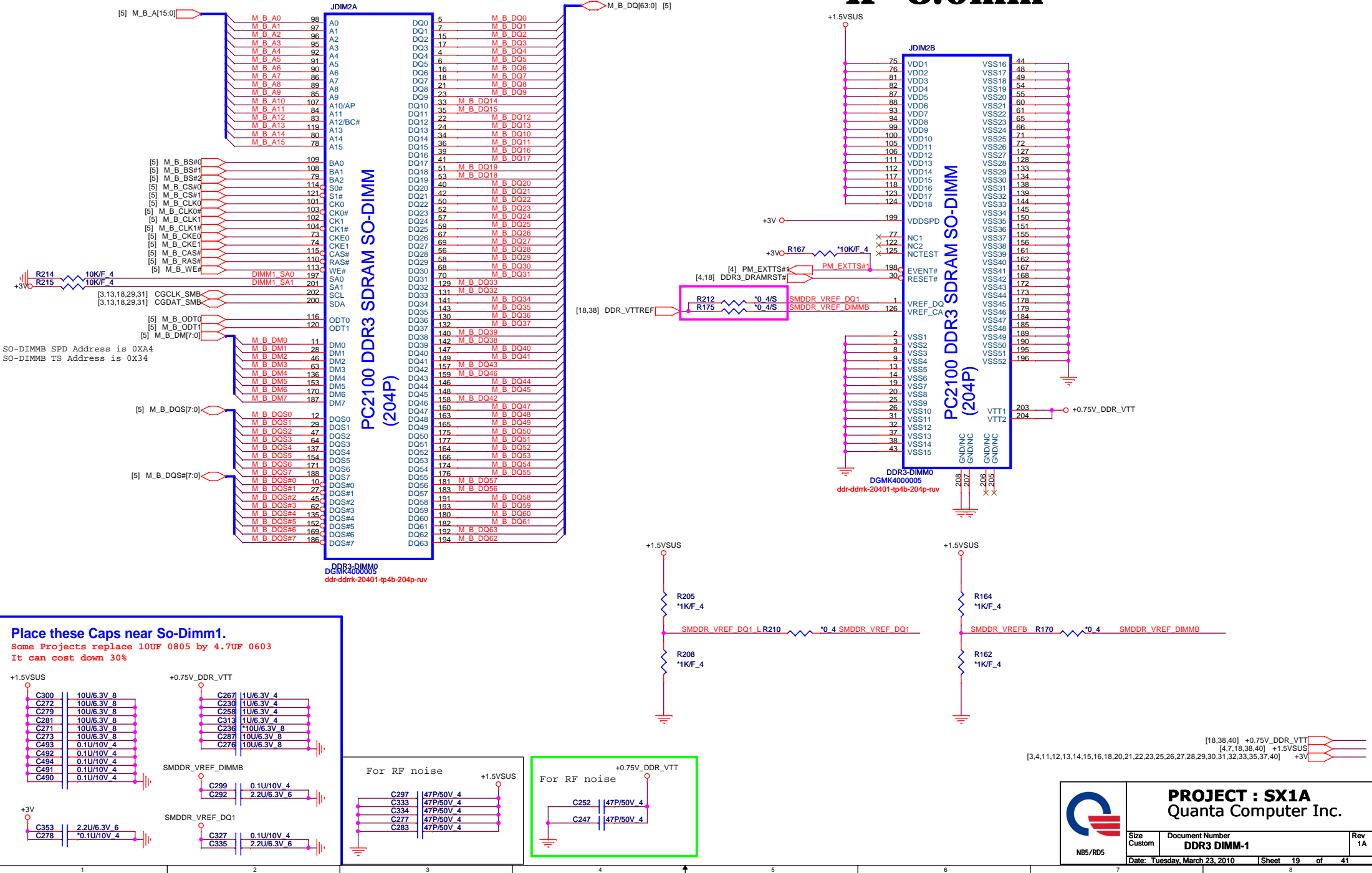
Size	Document Number	Rev
Custom	PCH 6/6 (GND)	1A
Date: Tuesday, March 23, 2010 Sheet 17 of 41		

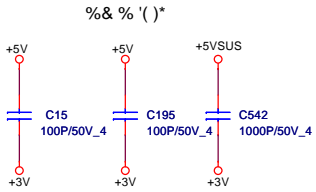
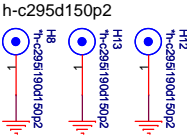
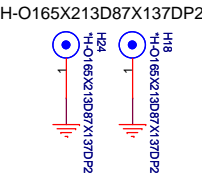
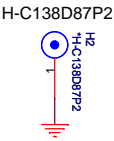
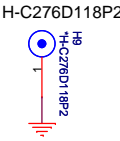
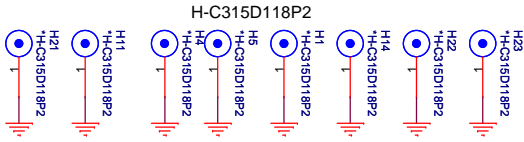
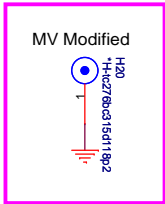
NB5/RD5

H=8.0mm



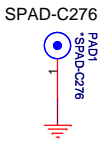
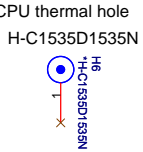
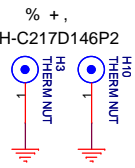
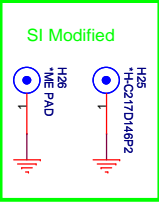
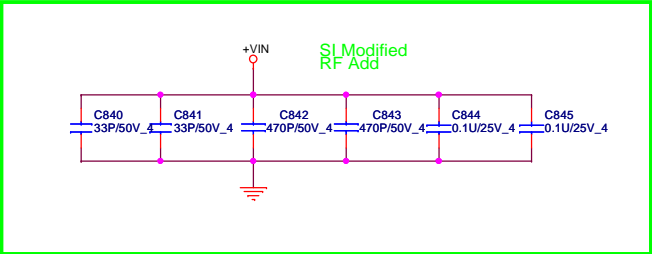
H=8.0mm



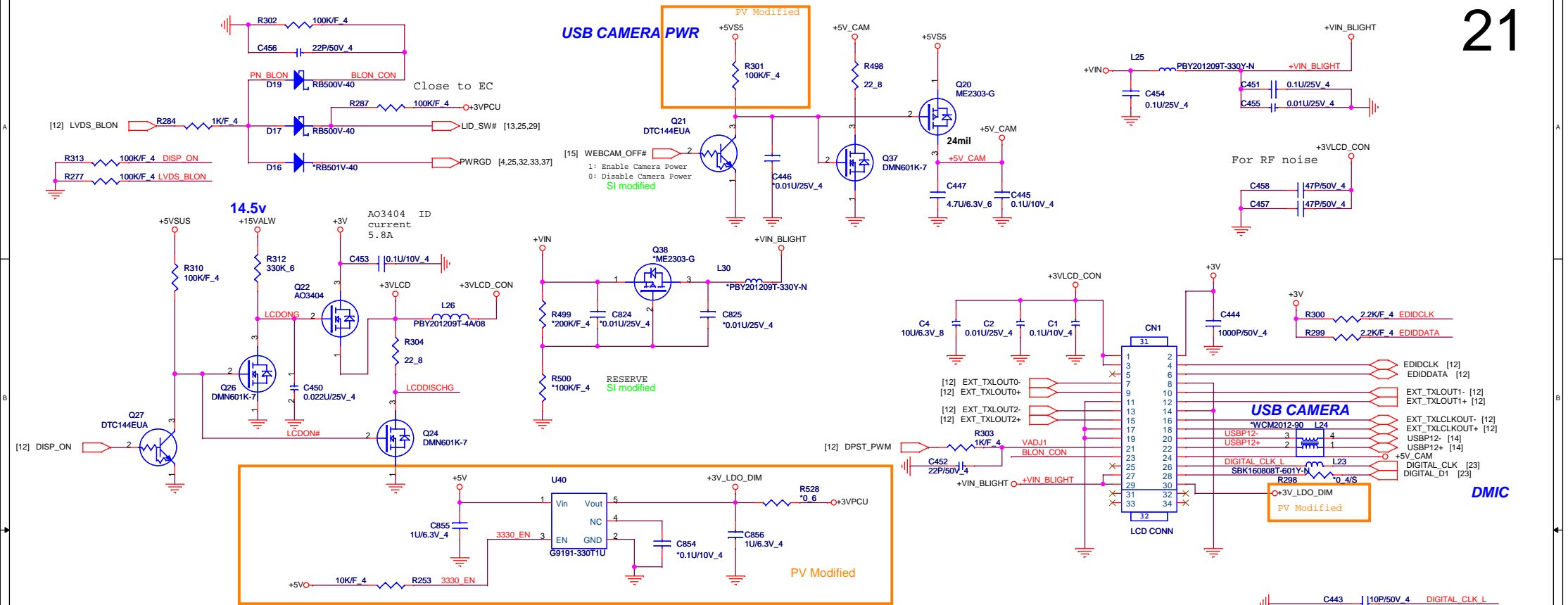


. / * , 0 \$ * 1

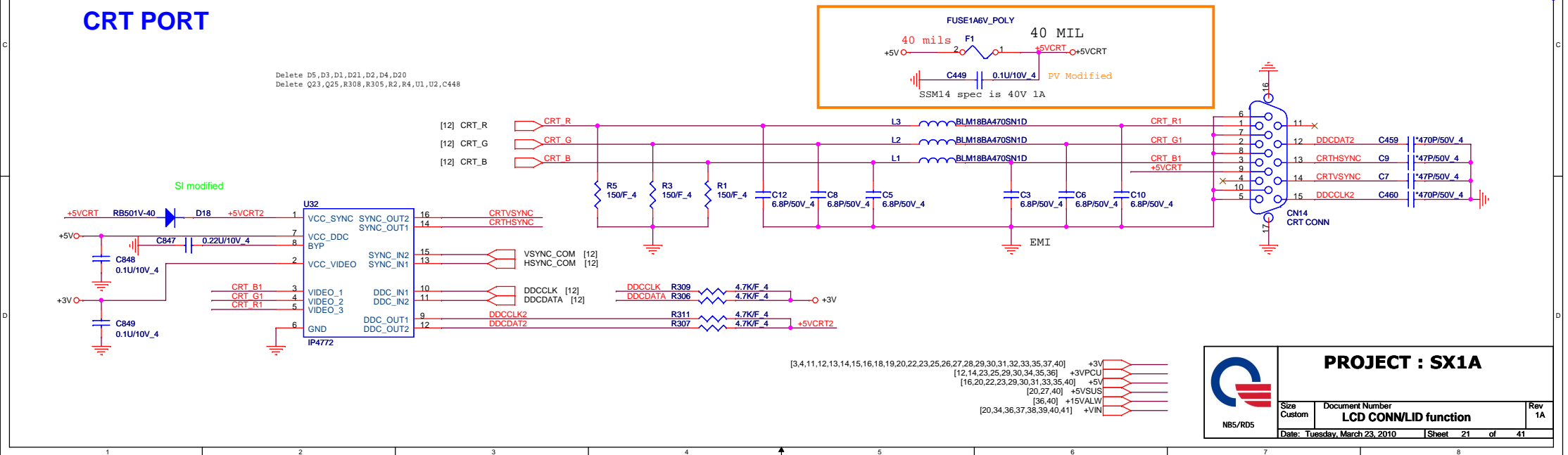
Delete U32,CN10,R253,C570C572,C573,C574,C578,C579,C 582,C583,C584,C586,C587C588



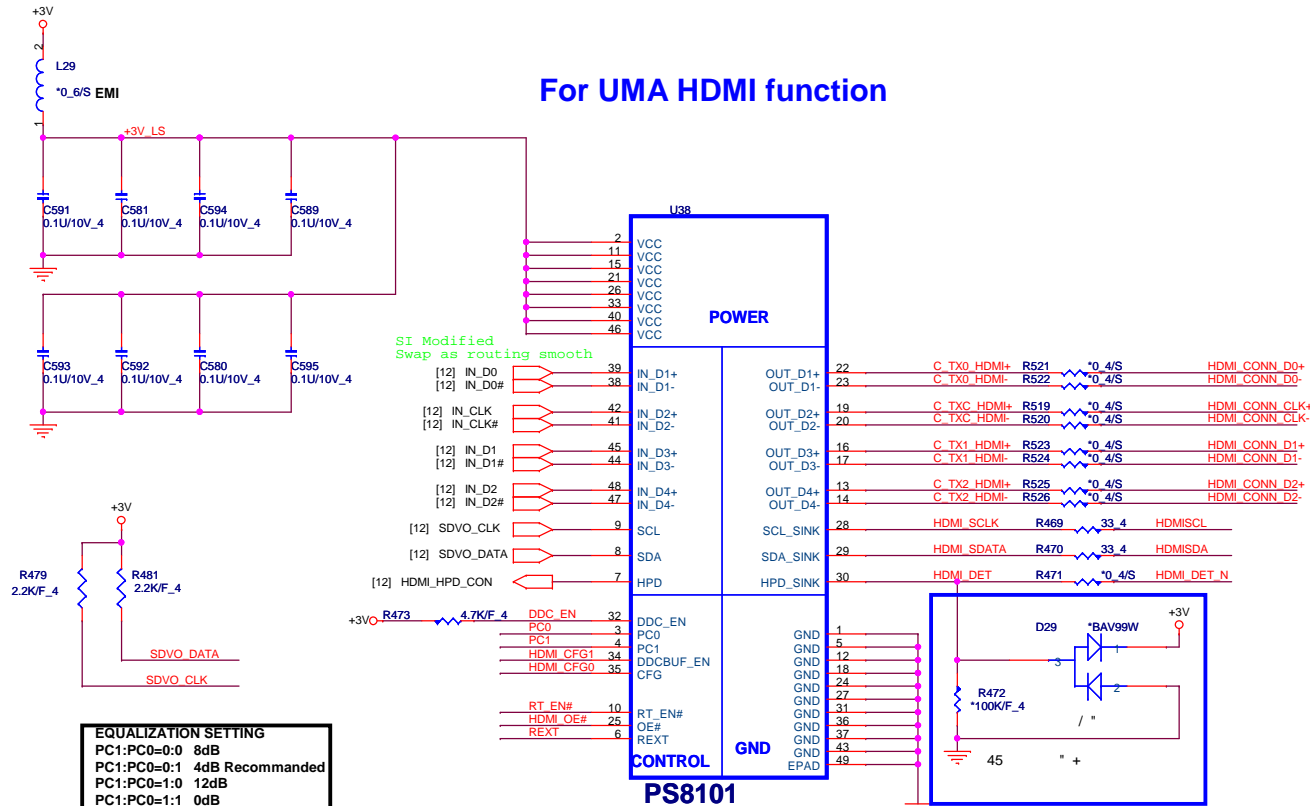
USB CAMERA PWR



CRT PORT

Delete D5,D3,D1,D21,D2,D4,D20
Delete Q23,Q25,R308,R305,R2,R4,U1,U2,C448

For UMA HDMI function



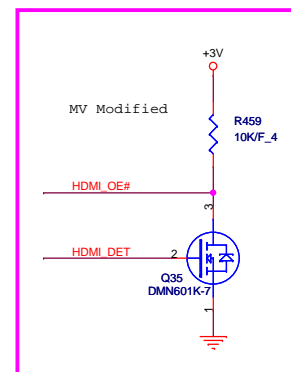
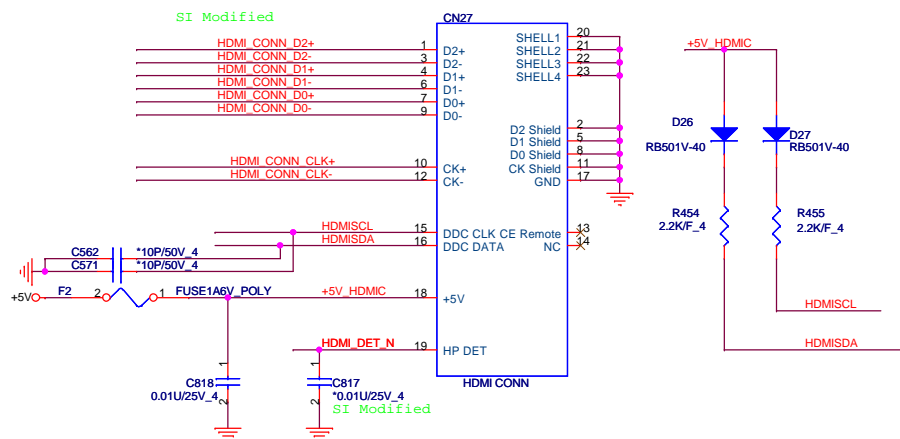
EQUALIZATION SETTING	
PC1:PC0=0:0	8dB
PC1:PC0=0:1	4dB Recommended
PC1:PC0=1:0	12dB
PC1:PC0=1:1	0dB

SCLZ/SDAZ Low-level input/output Voltage

CFG1:CFG0=0:0	VIL:<0.4V VOL:0.6V (Default)
CGF1:CGF0=0:1	VIL:<0.36V VOL:0.55V
CGF1:CGF0=1:0	VIL:<0.44V VOL:0.65V
CGF1:CGF0=1:1	VIL:<0.36V VOL:0.6V

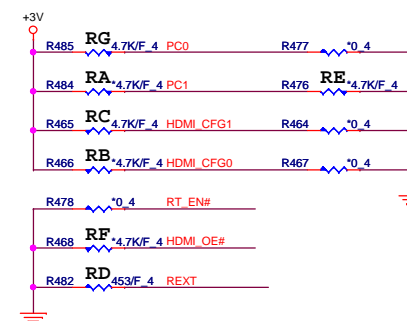
Vendor:PDT P/N:AL008101000

Vendor:PIM P/N:ALP411LS004



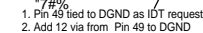
pin \ vendor	PIM/signal	PDT/signal
32	DDC_EN	DDC_EN
3	OC_0	PC0
4	OC_1	PC1
34	EQ_0	DDCBUF_EN
35	EQ_1	CFG
10	OC_3	RT_EN#
25	OE#	OE#
6	OC_2 (REXT)	REXT

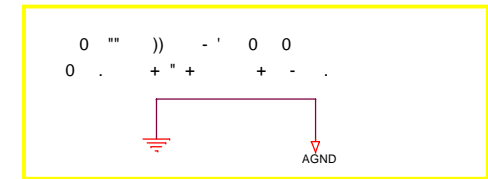
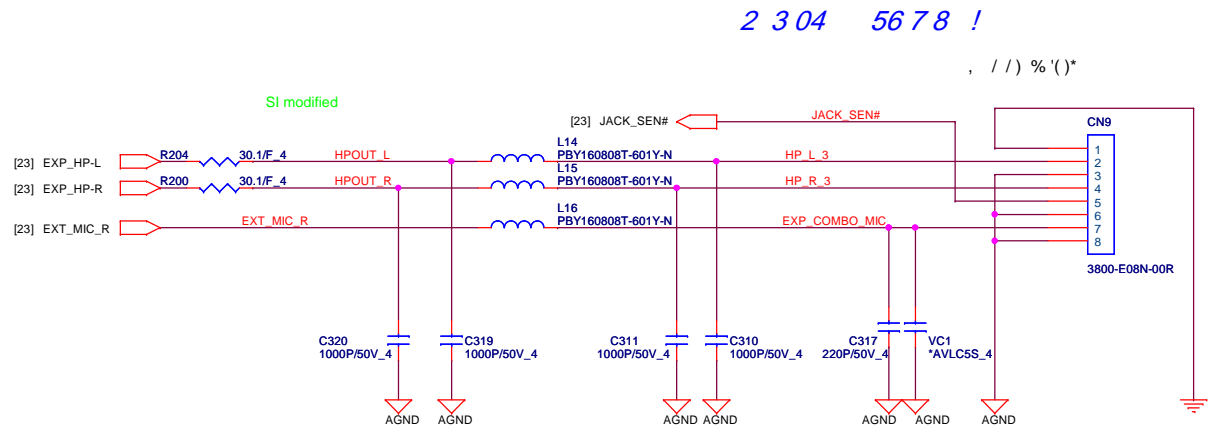
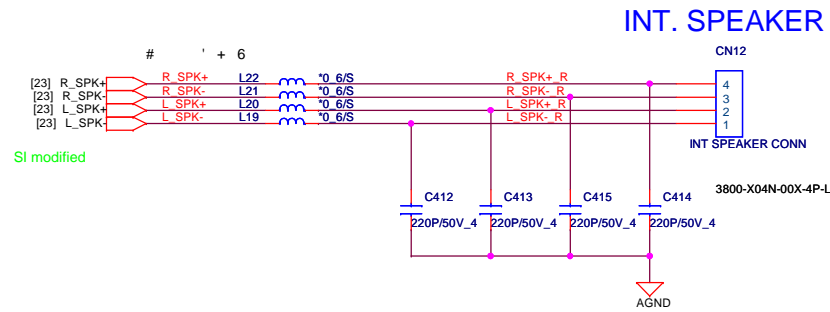
HDMI PORT



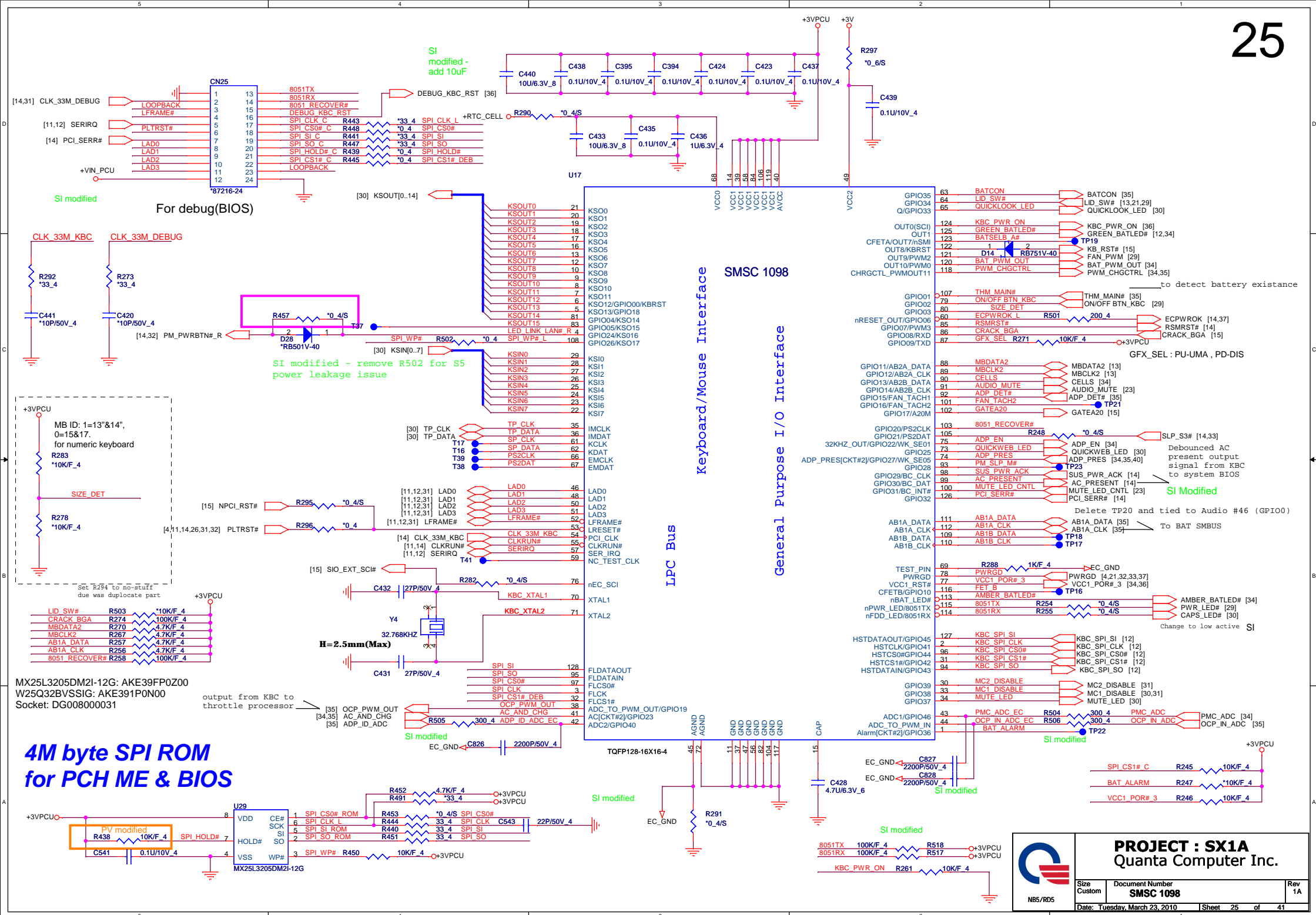
Signals		PDT	IM
PC1	RA	NC	NC
HDMI_CFG0	RB	NC	NC
HDMI_CFG1	RC	4.7K	NC
REXT	RD	453	4.7K
PC1	RE	NC	4.7K
HDMI_OE#	RF	NC	NC
PC0	RG	4.7K	4.7K

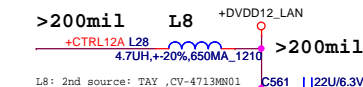
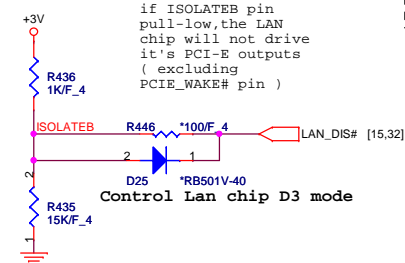
PROJECT : SX1A



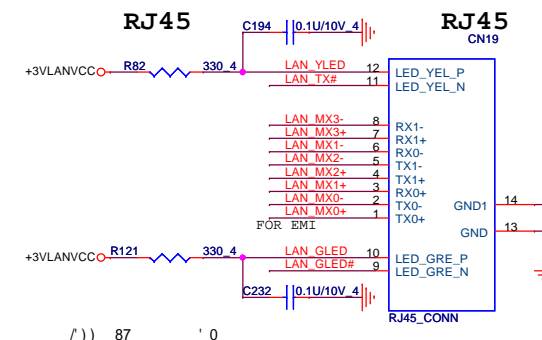
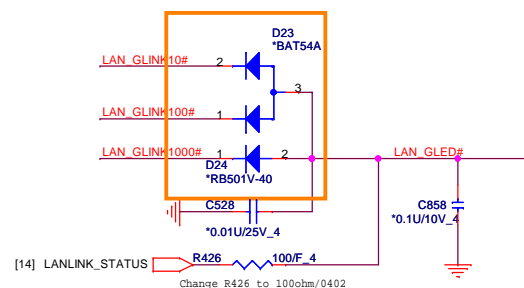


Note: JACK_SEN# is electrically floating when no jack is inserted and shorted to ground when jack is present.

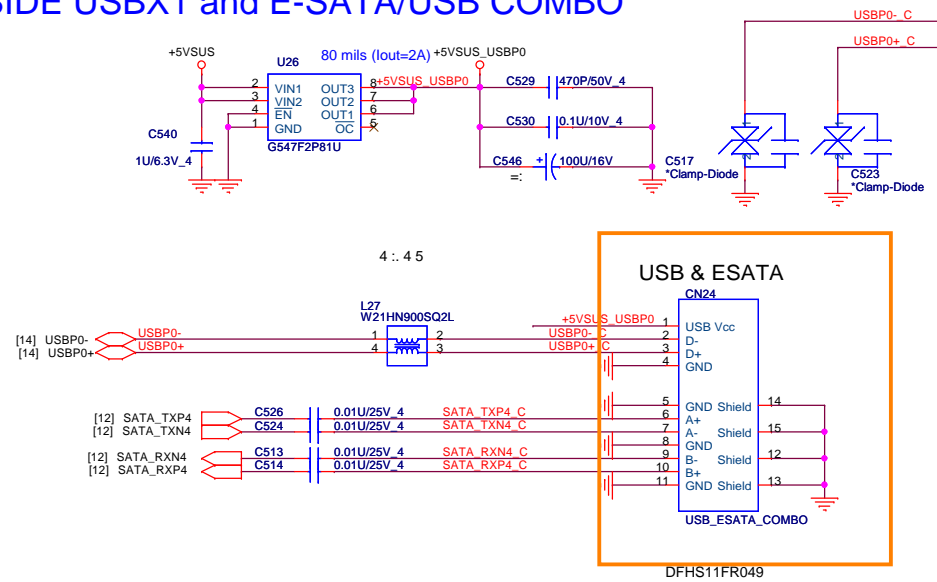




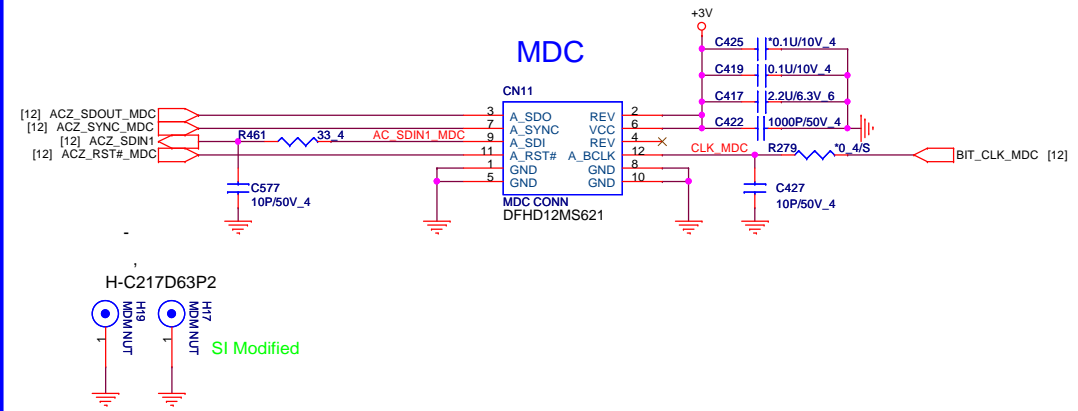
Lan and MDC Con.



LEFT SIDE USBX1 and E-SATA/USB COMBO

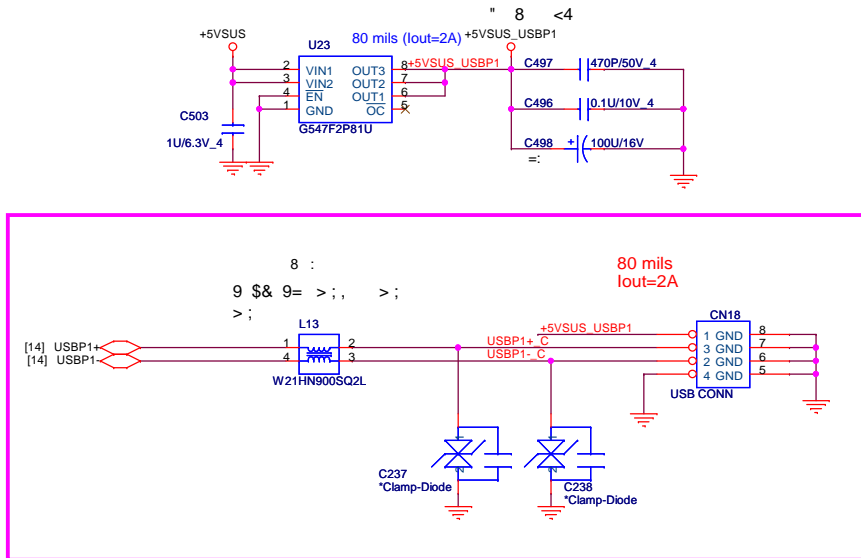


Modem CONN

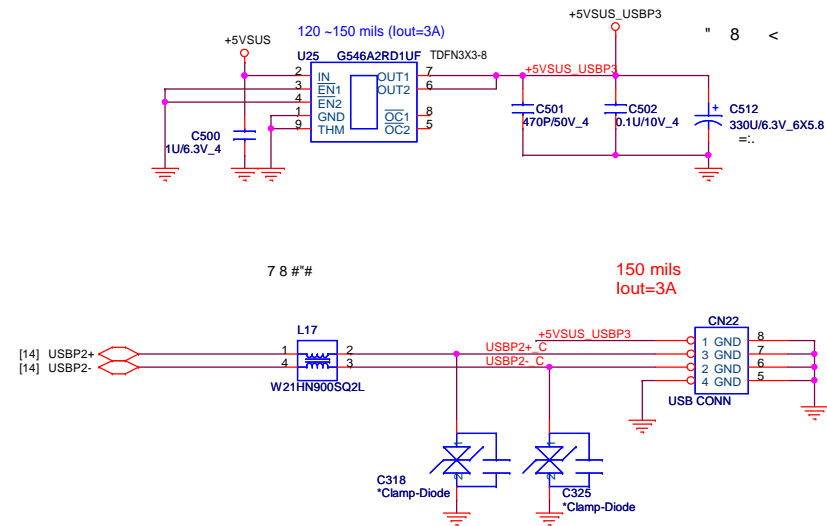


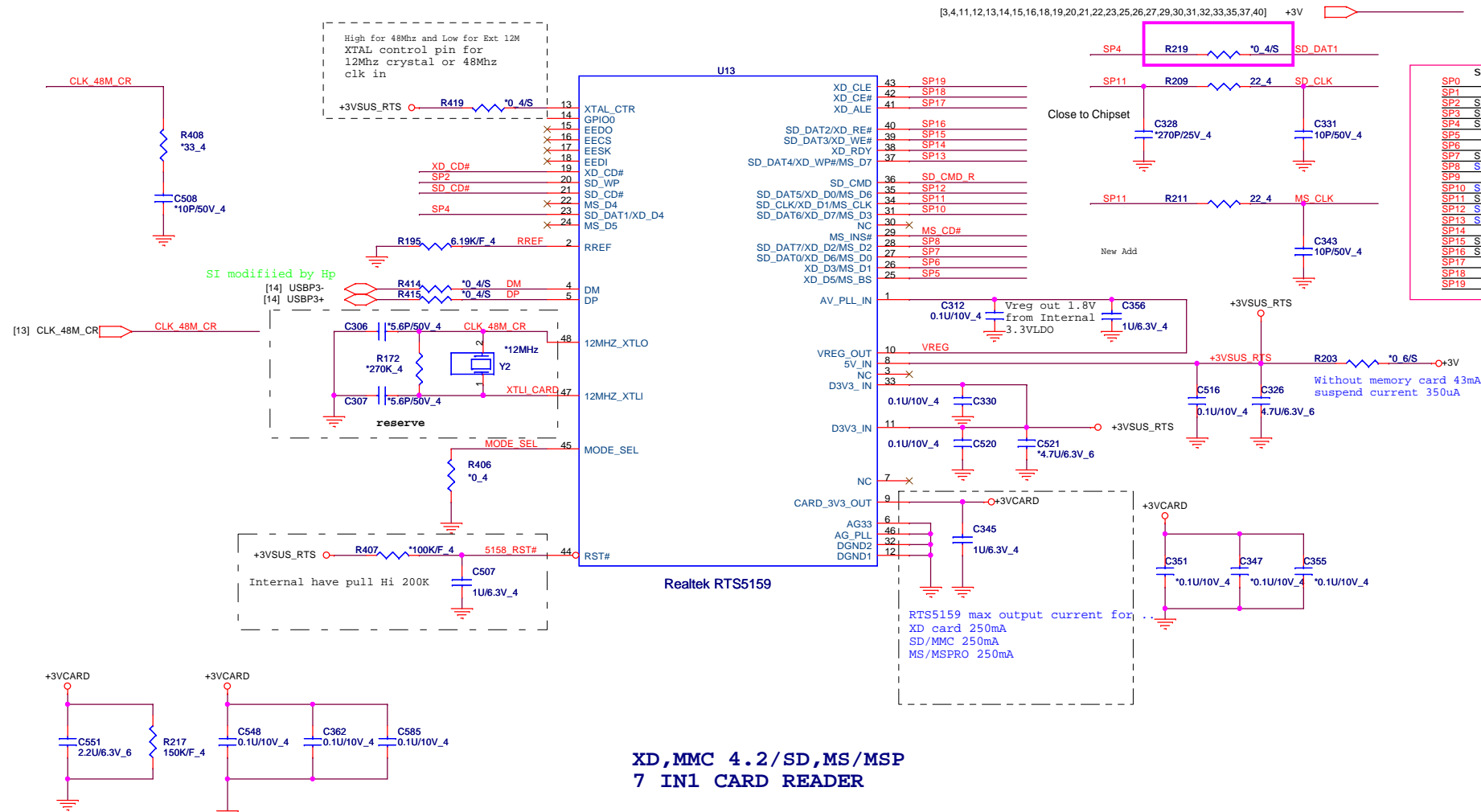
RIGHT SIDE USBX2(Powered USB *1)

Standard USB



Powered USB

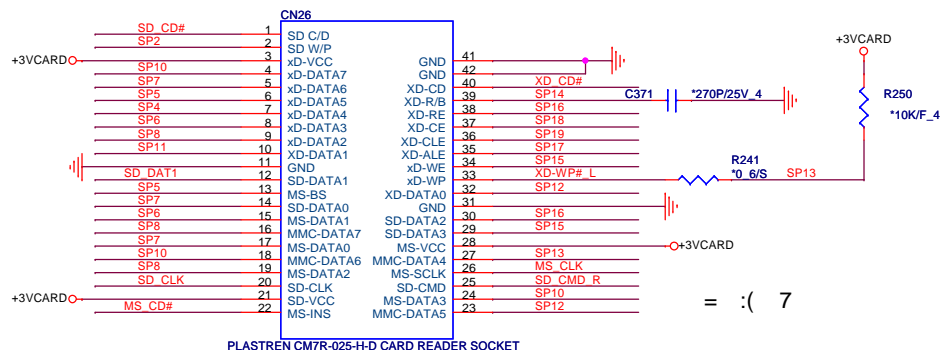




Note:

SP0	SD/MMC 4.2	MS	XD
SP1	SD WP		XD CD#
SP2	SD CD#		
SP3	SD DAT1		
SP4		MS BS	XD D4
SP5		MS D1	XD D5
SP6		MS D2	XD D3
SP7	SD DAT0	MS D0	XD D6
SP8	SD DAT7/MMC DAT7	MS D2	XD D2
SP9		MS INS#	
SP10	SD DAT6/MMC DAT6	MS D3	XD D7
SP11	SD CLK	MS SCLK	XD D1
SP12	SD DAT5/MMC DAT5		XD D0
SP13	SD DAT4/MMC DAT4		XD WP#
SP14			XD R/B#
SP15	SD DAT3		XD WE#
SP16	SD DAT2		XD RE#
SP17			XD ALE
SP18			XD CE#
SP19			XD CLE

XD, MMC 4.2 / SD, MS / MSP 7 IN1 CARD READER

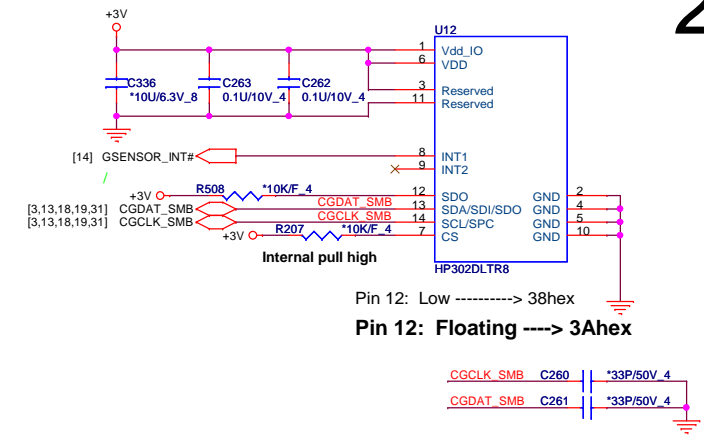


PLASTREN CM7R-025-H-D CARD READER SOCKET

REVERSE TYPE

PROJECT : SX1A
Quanta Computer Inc.

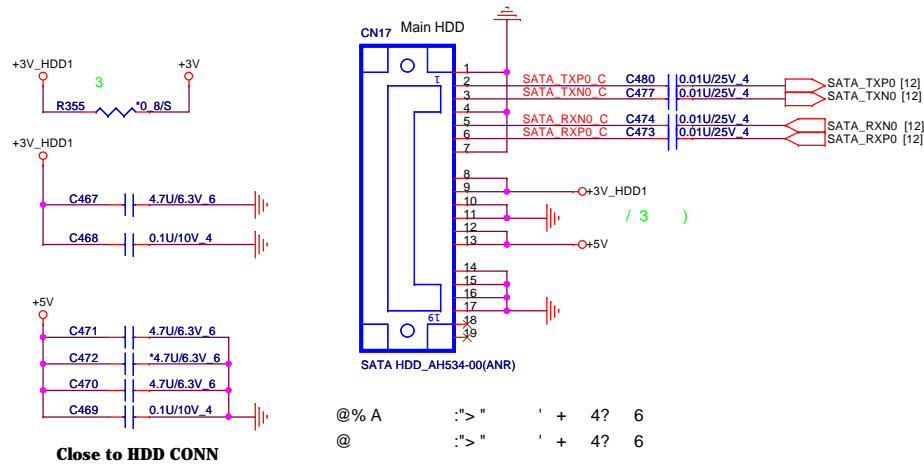
Accelerometer Sensor



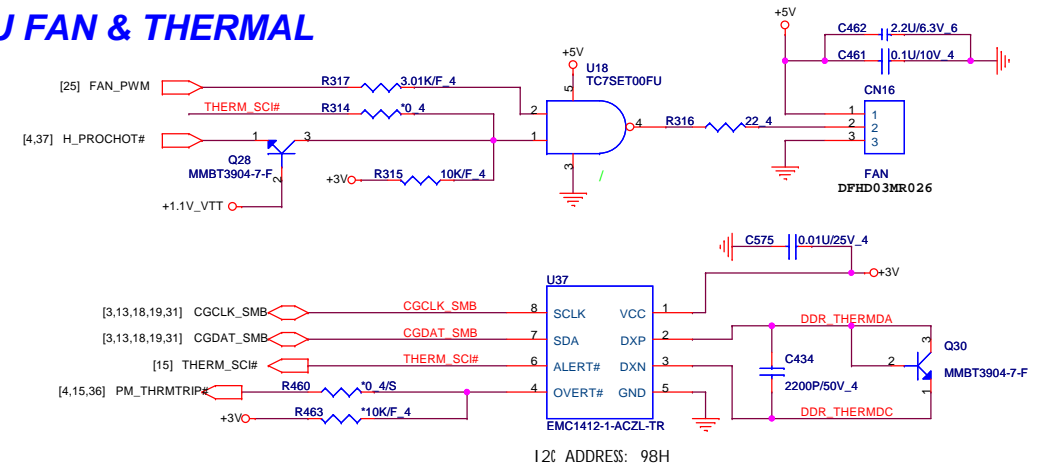
Per pin:1 Amp

+5V: 1.5 A(2 Pin)

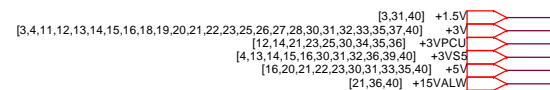
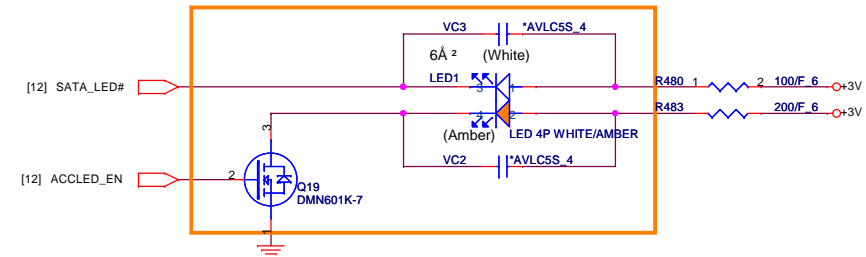
Gnd : (7 Pin)



CPU FAN & THERMAL



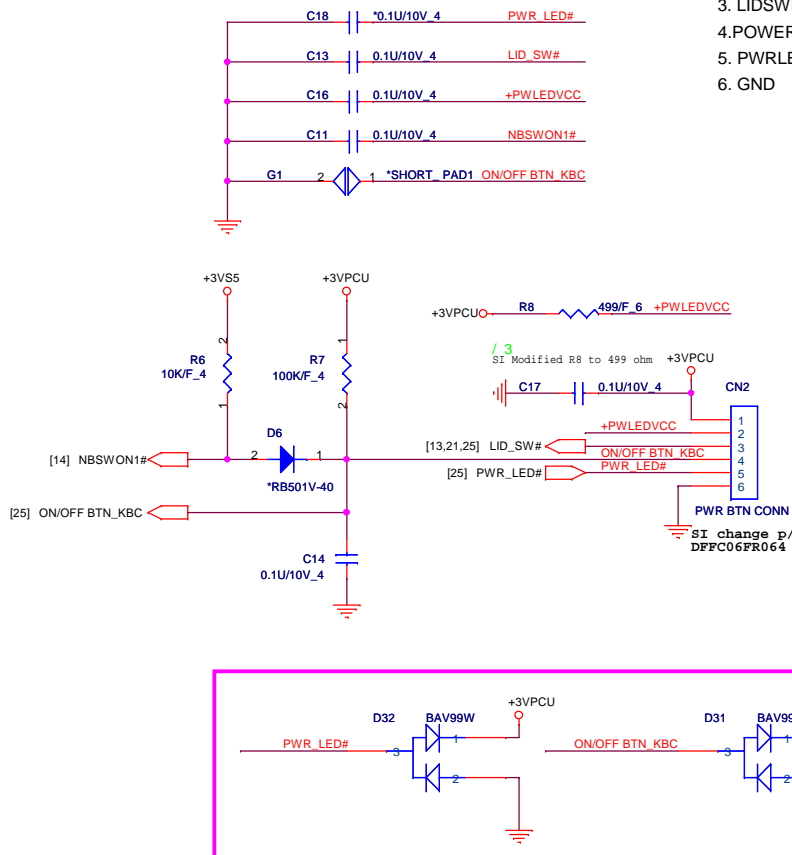
LED



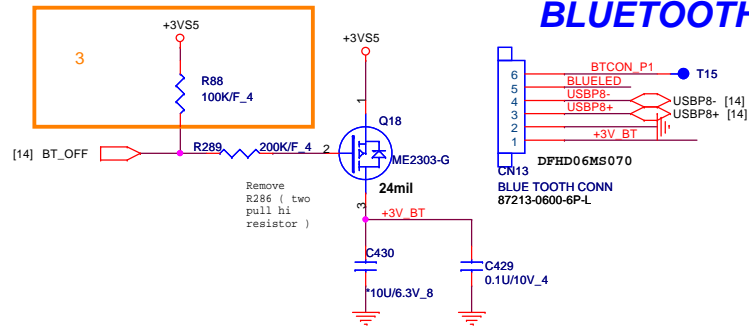
SATA HDD CONNECTOR

POWER BOTTON CONNECT

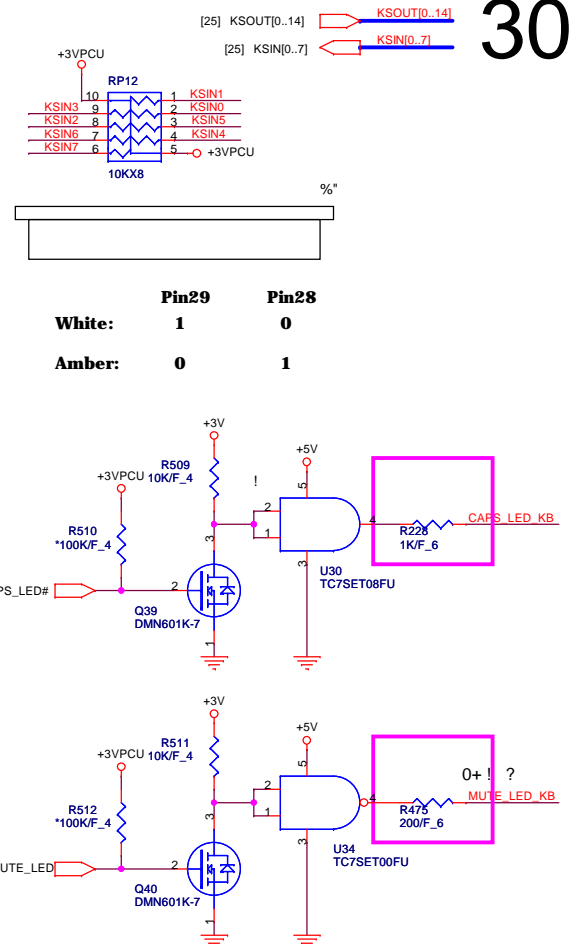
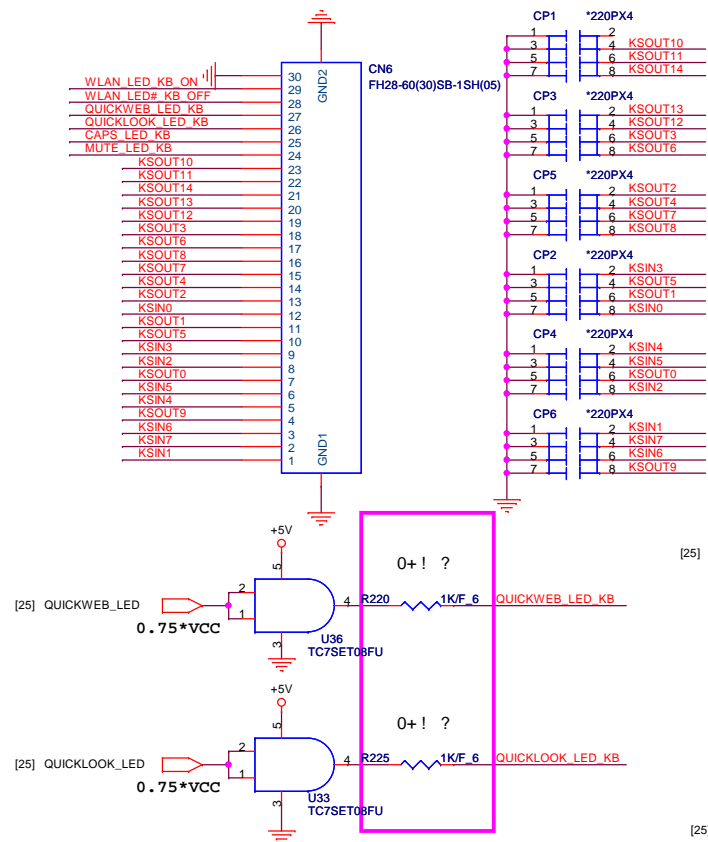
1. +3VPCU(LIDSWITCH PWR)
2. LEDVCC(+3VPCU)
3. LIDSWITCH
4. POWERON#
5. PWRLED#
6. GND



BLUETOOTH

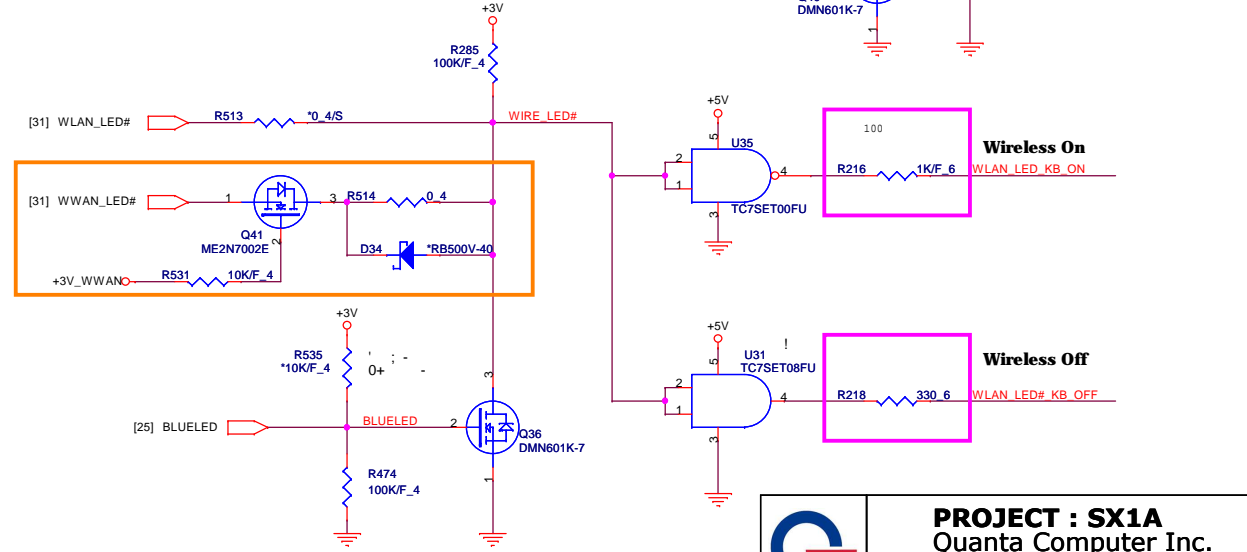
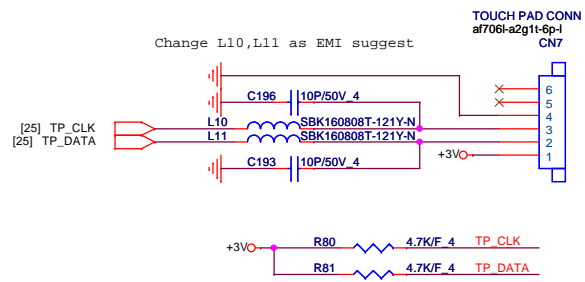


KEYBOARD CONNECTOR.

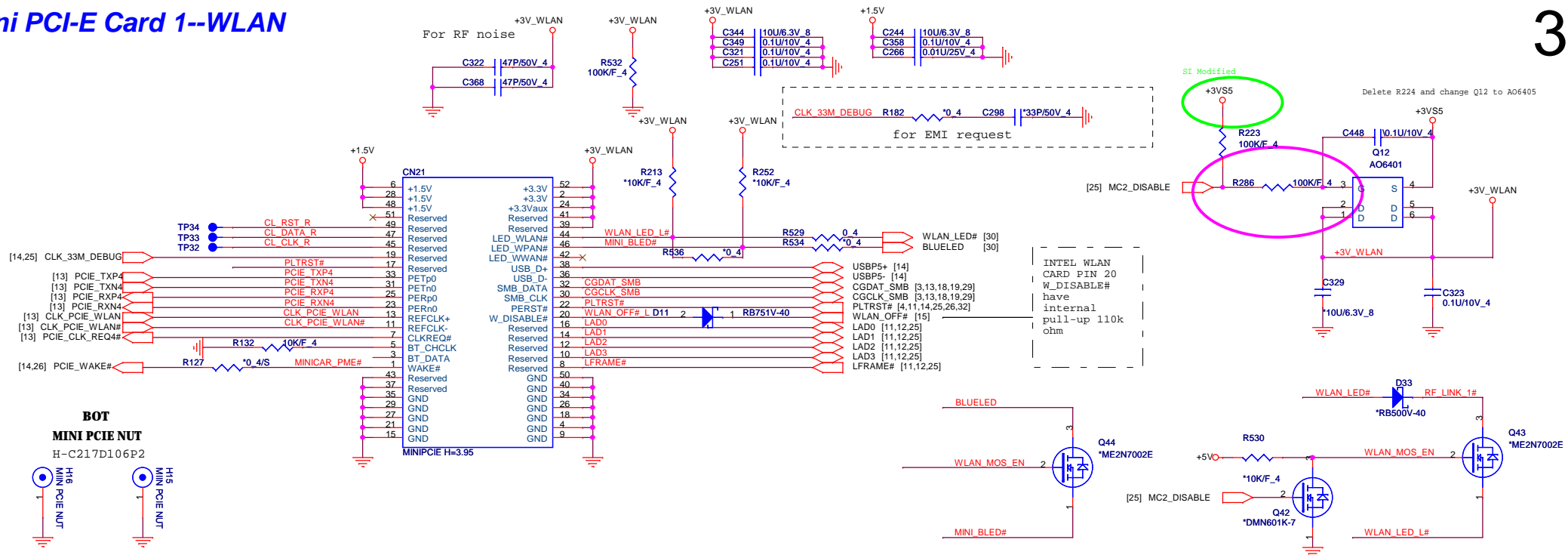


TOUCH PAD CONNECTOR

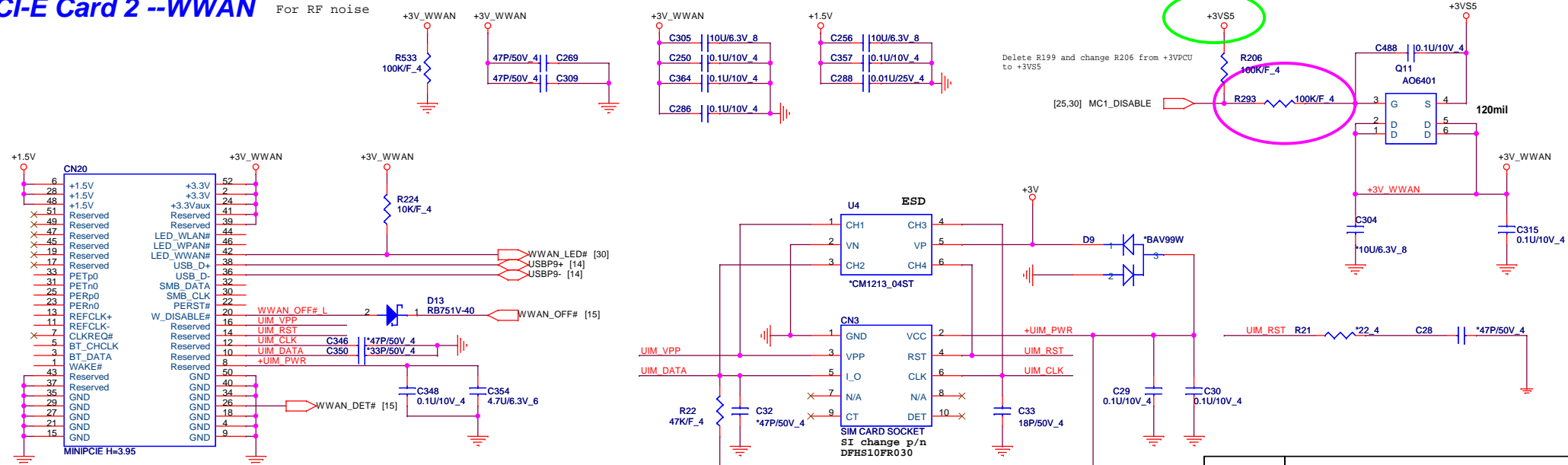
Pin1	VDD
Pin2	DATA
Pin3	CLK
Pin4	GND
Pin5	X
Pin6	X



Mini PCI-E Card 1--WLAN



Mini PCI-E Card 2 --WWAN



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Quanta Computer Inc.

Size	Document Number	Rev
Custom	MINI PCIE CONN X2	1A
Date: Tuesday, March 23, 2010	Sheet 31 of 41	

[3,4,11,12,13,14,15,16,18,19,20,21,22,23,25,26,27,28,29,30,32,33,35,37,40]

[16,20,21,22,23,29,30,33,35,40]

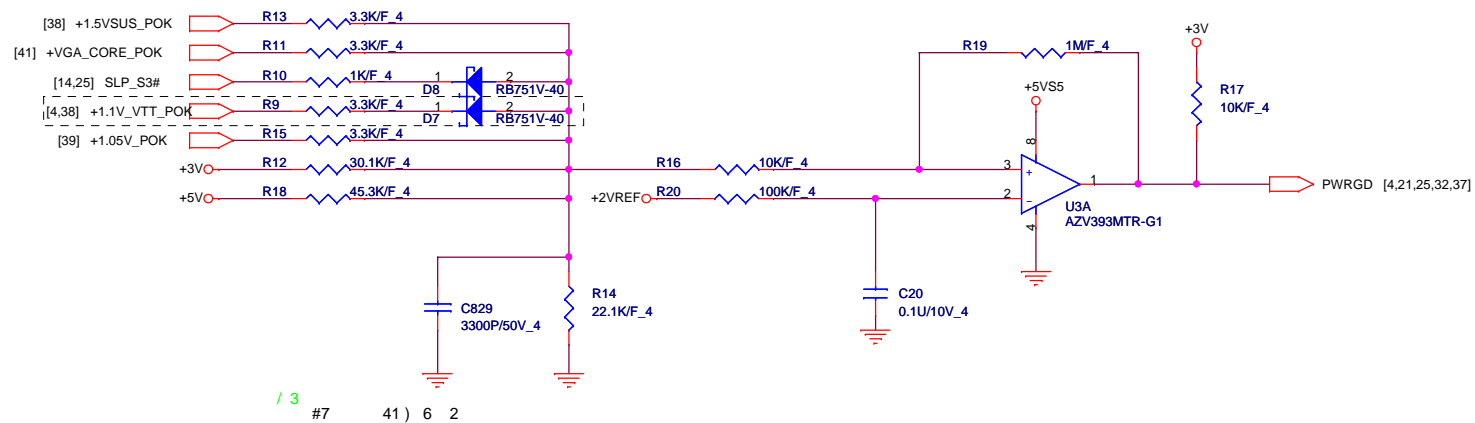
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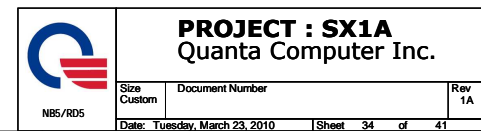
[16,20,21,22,23,29,30,33,35,40]

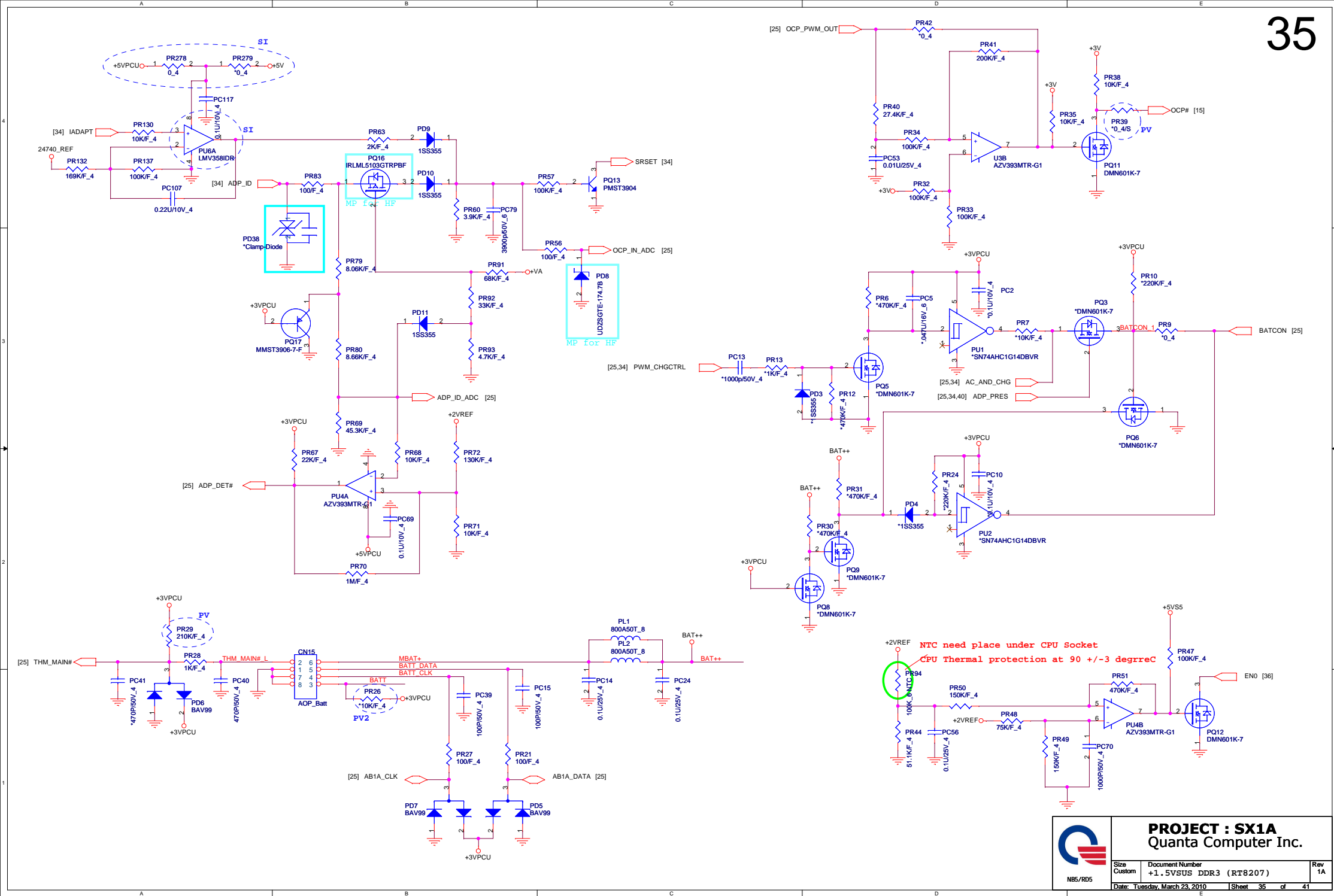


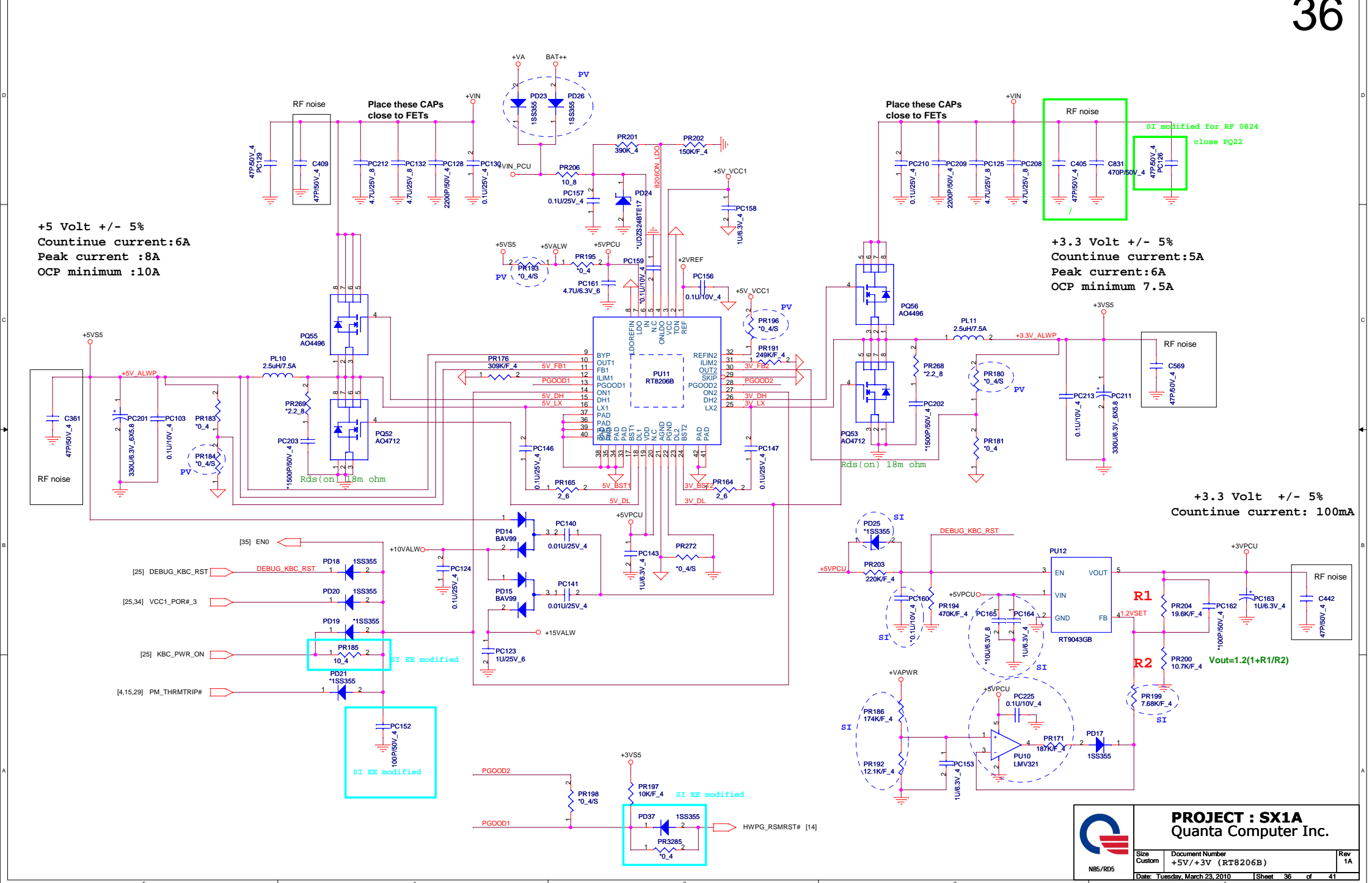
XDP_TDO	R68	*0.4	PCH_JTAG_TDI
XDP_TDI	R70	*0.4	PCH_JTAG_TDO
XDP_TCLK	R72	*0.4	PCH_JTAG_TCK
XDP_TMS	R71	*0.4	PCH_JTAG_TMS












+5 Volt +/- 5%
Countinue current:6A
Peak current :8A
OCP minimum :10A

+3.3 Volt +/- 5%
Countinue current:5A
Peak current:6A
OCP minimum 7.5A

+3.3 Volt +/- 5%
Countinue current: 100mA



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Quanta Computer Inc.

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Custom	+5V/+3V (RT8206B)	1A
Date: Tuesday, March 23, 2010		Sheet 36 of 41

